

FIGURE 1B

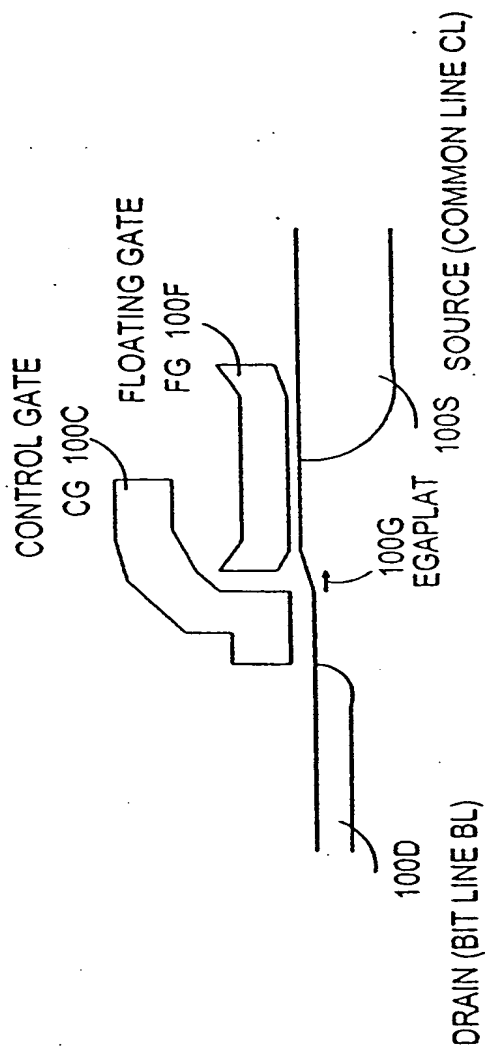


FIGURE 1A

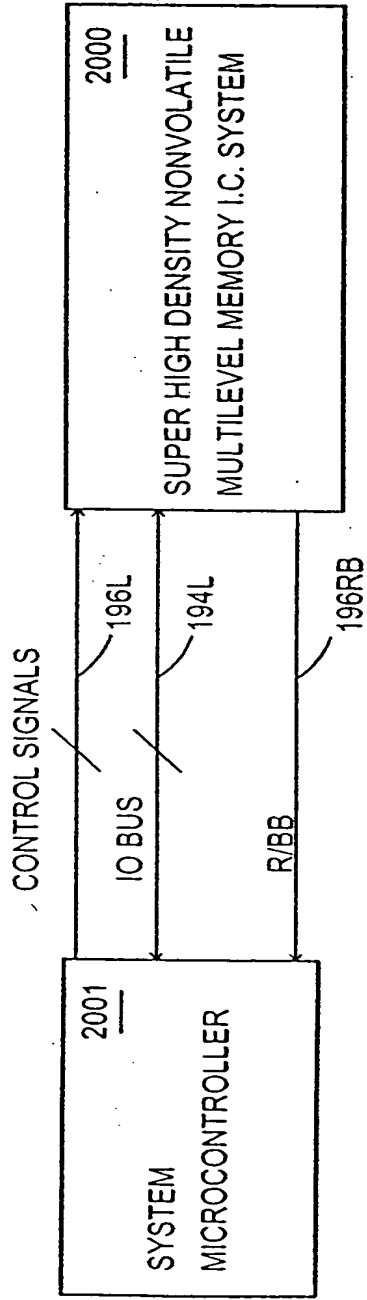


FIGURE 1C

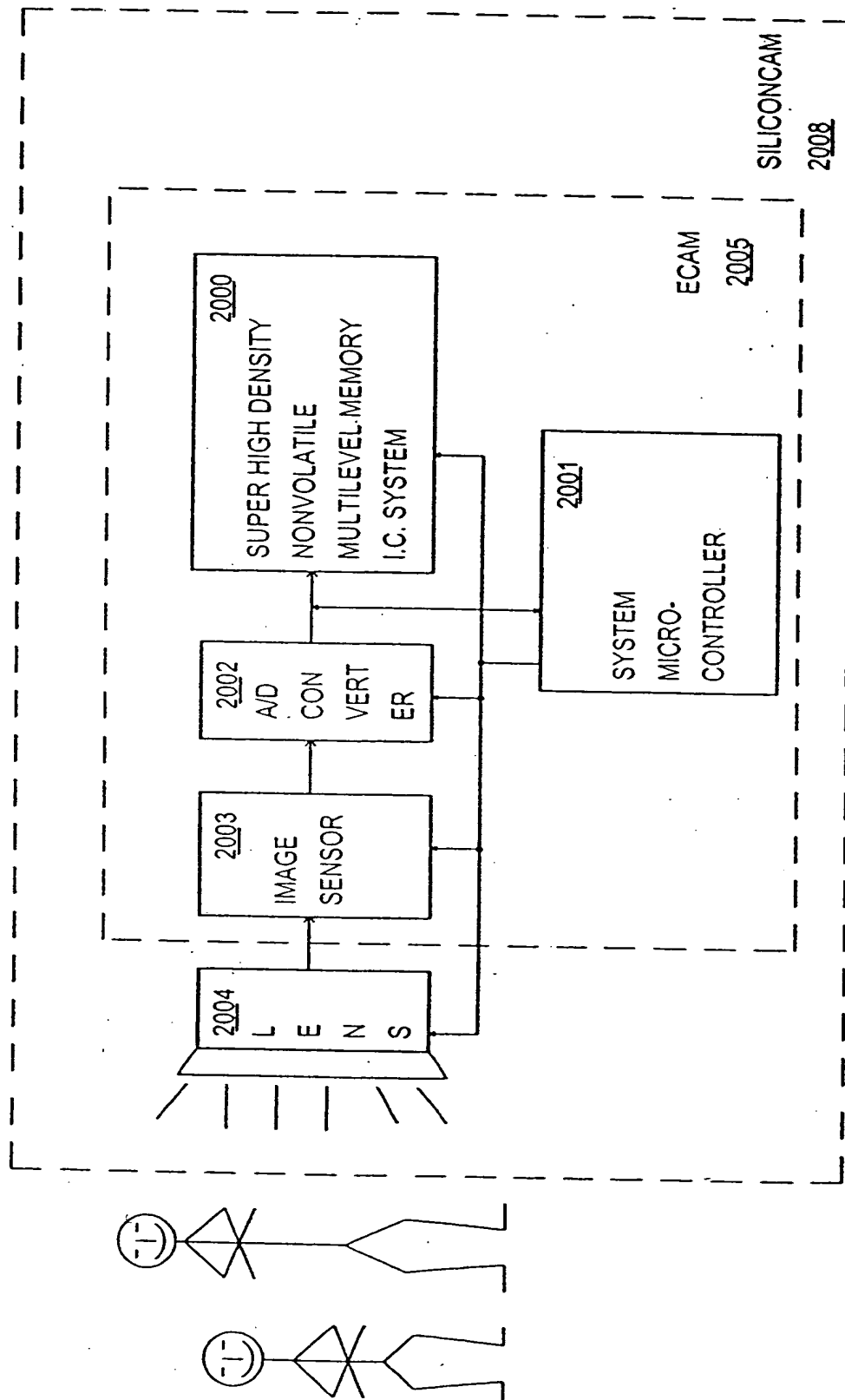


FIGURE 1D

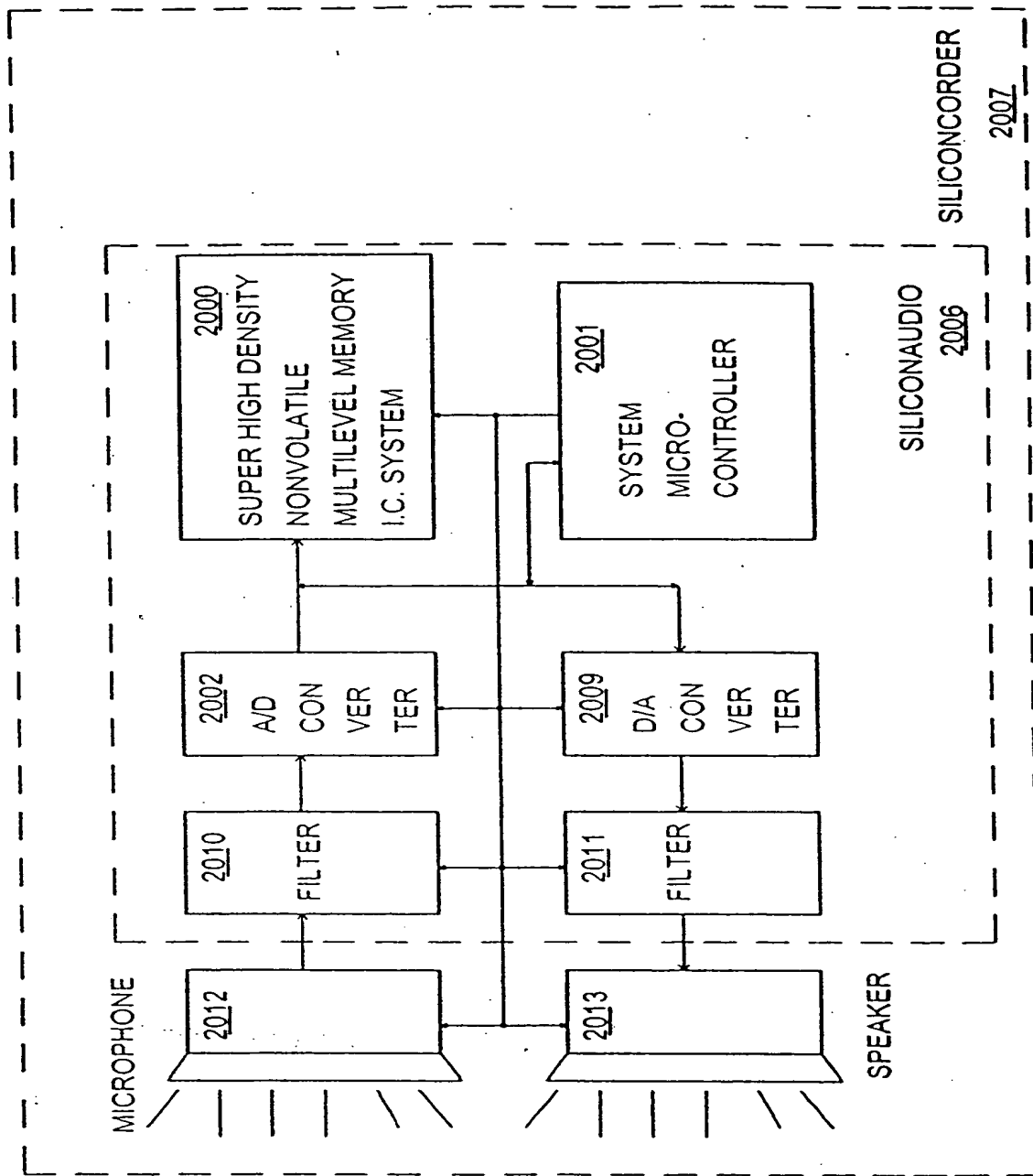
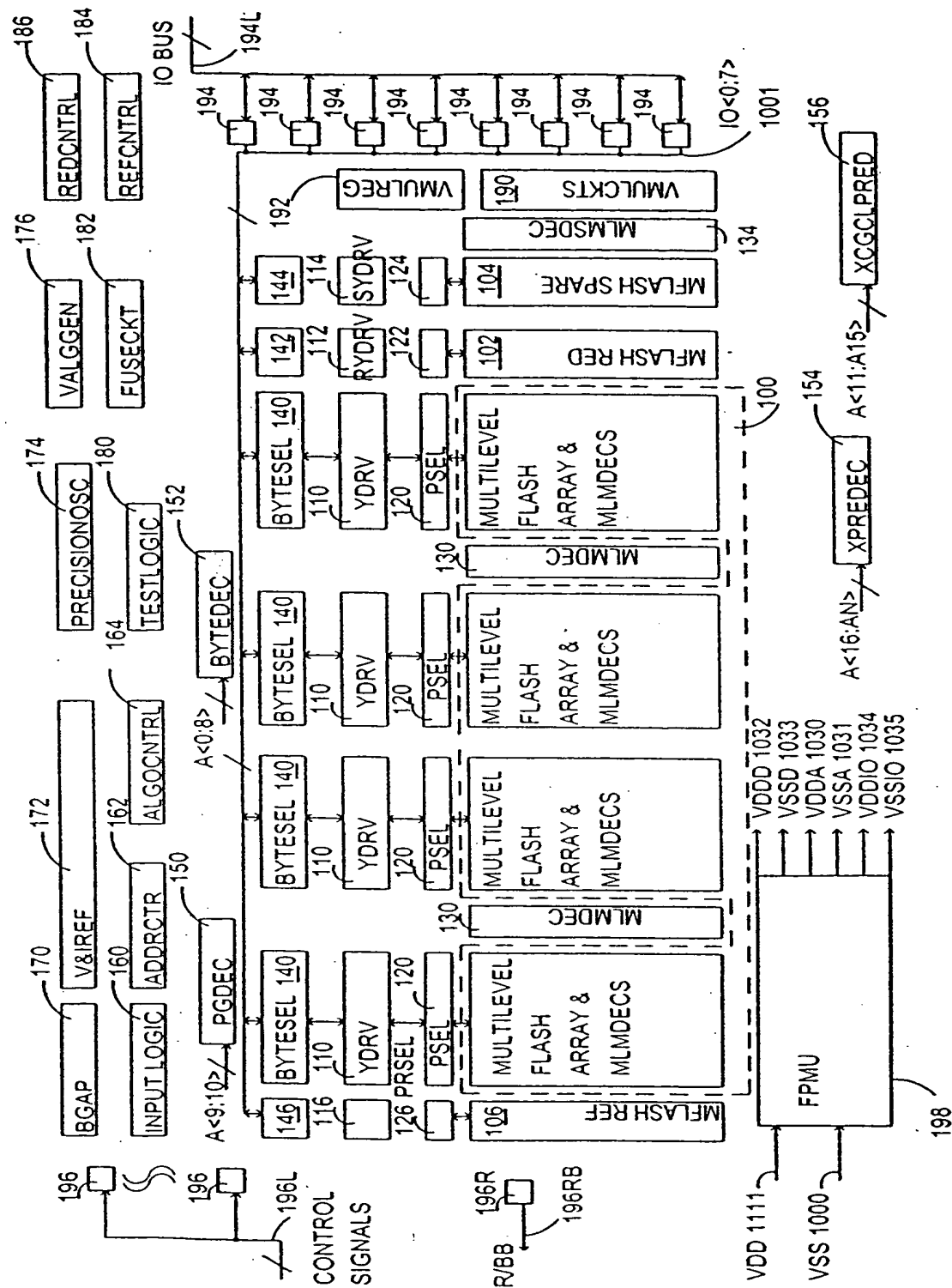


FIGURE 1E



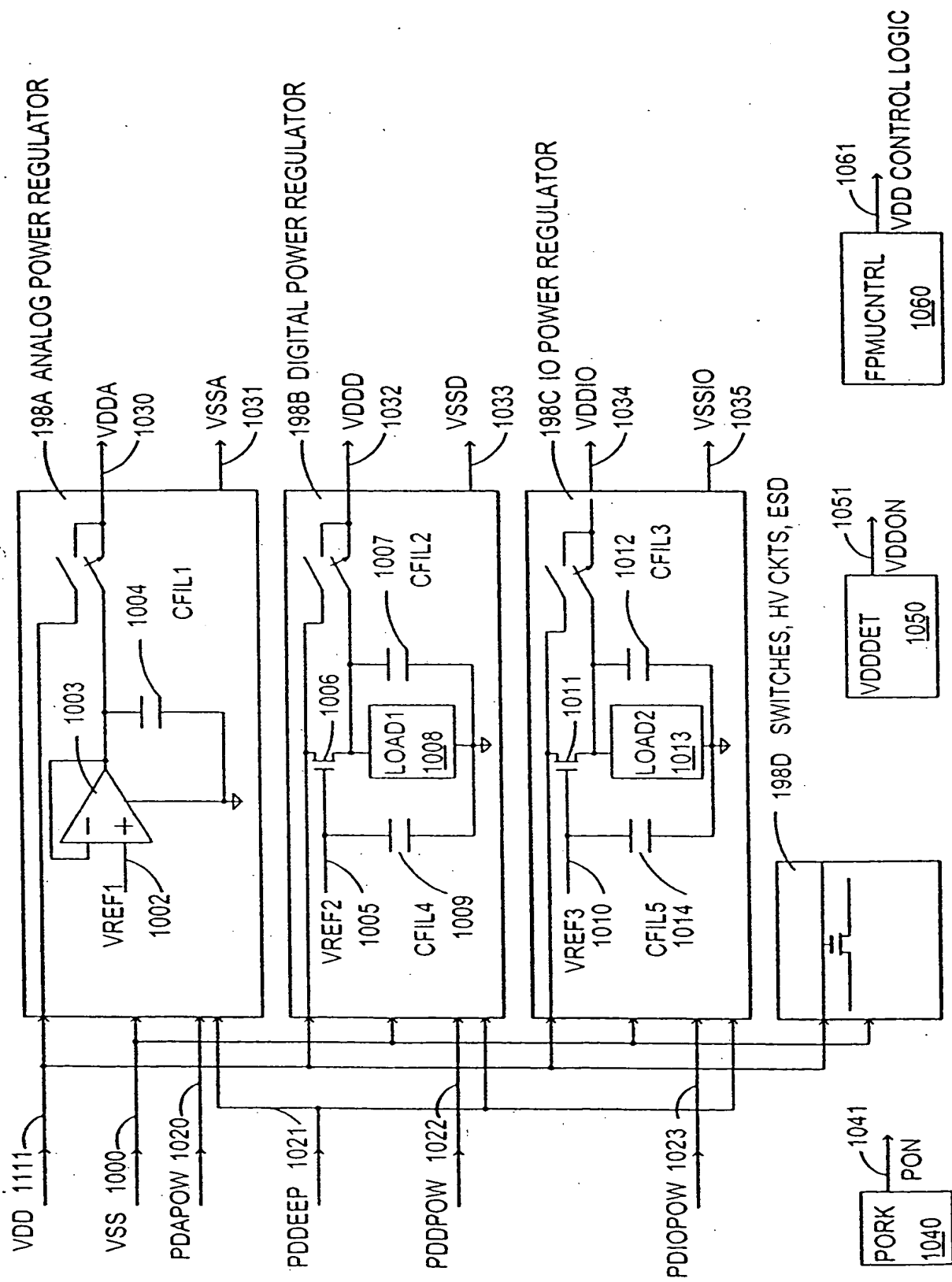


FIGURE 2B

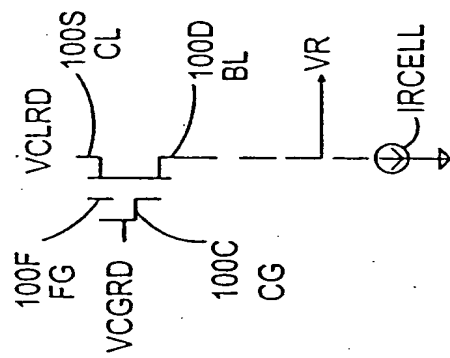


FIGURE 2C

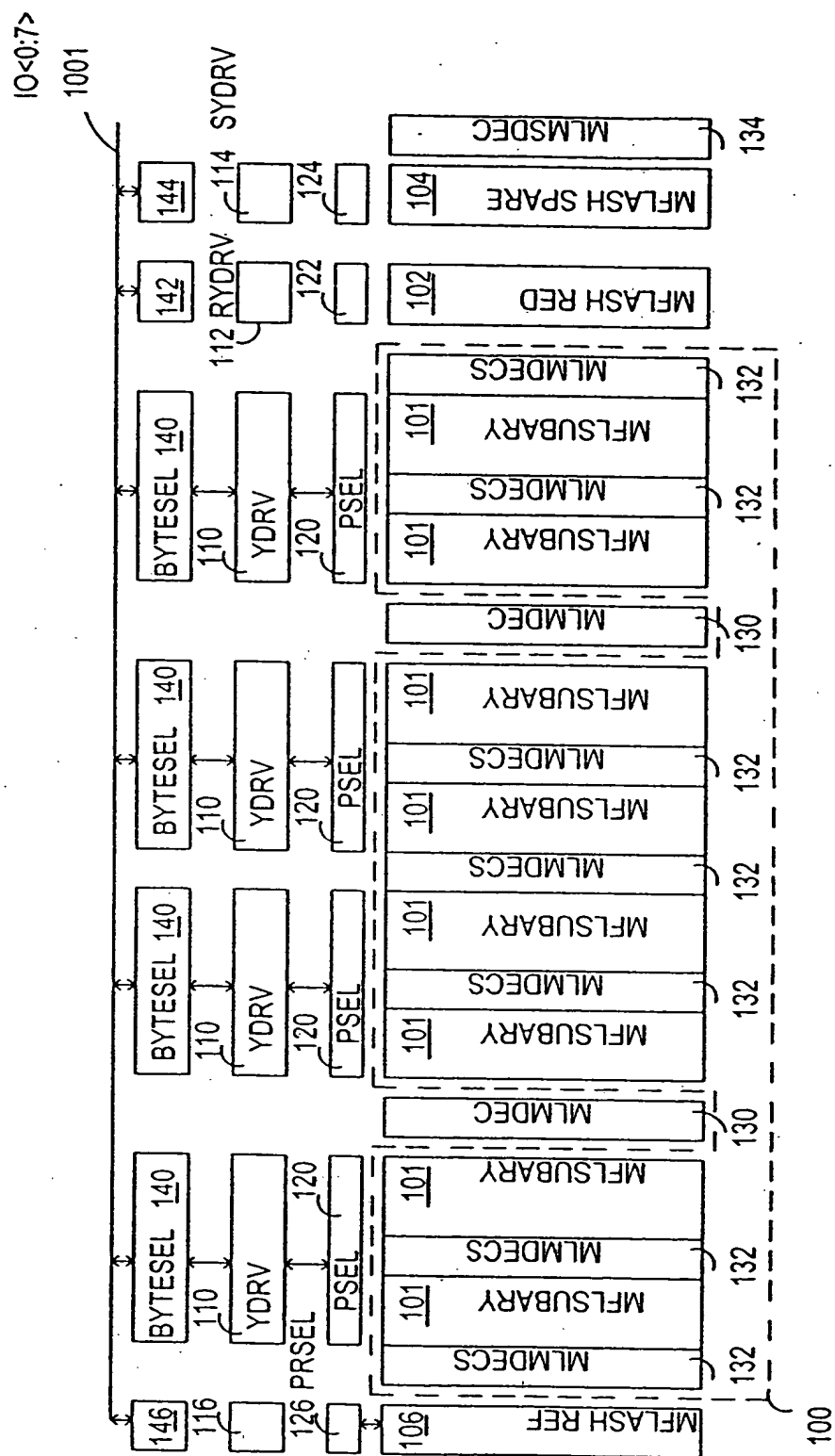


FIGURE 3A

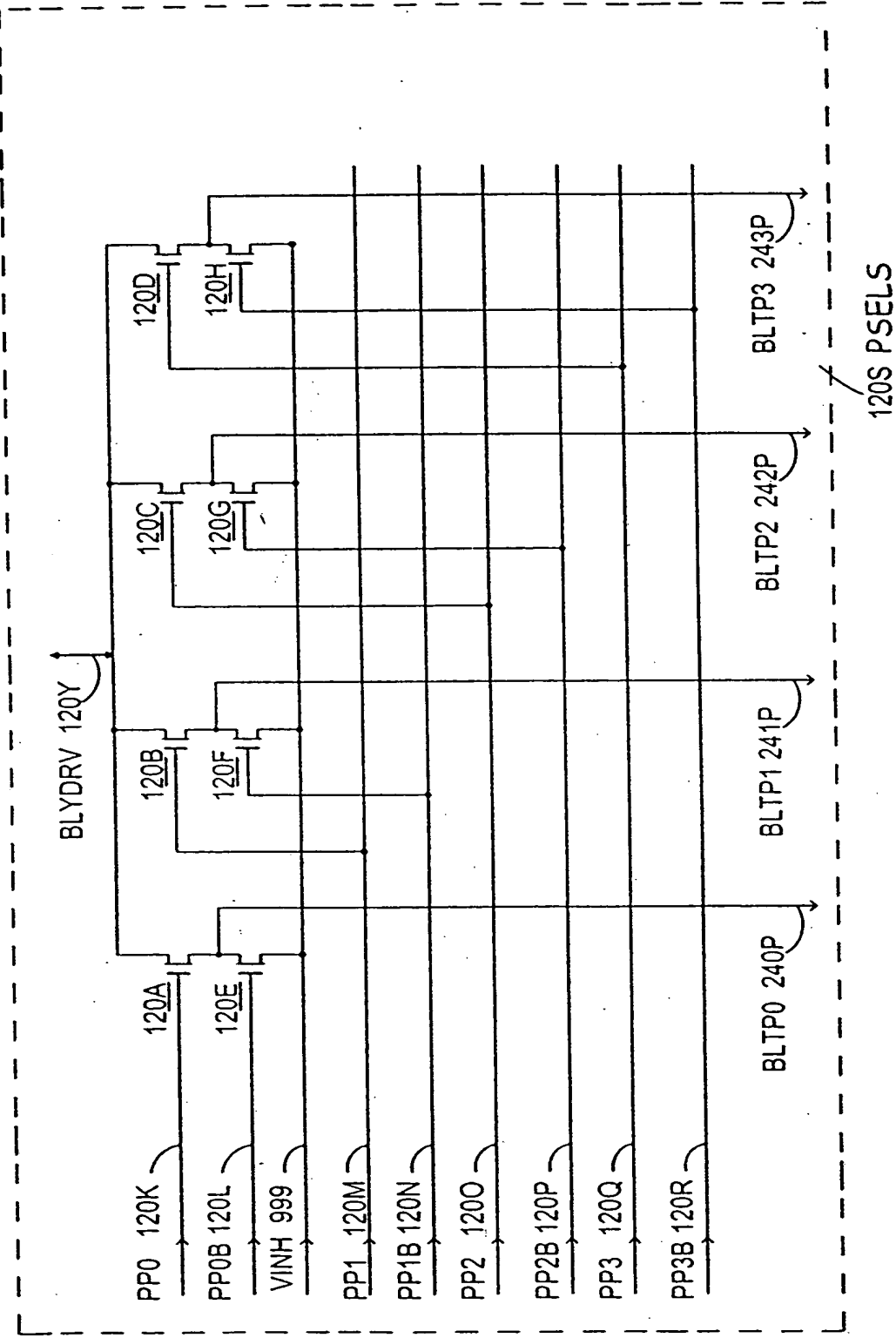


FIGURE 3B

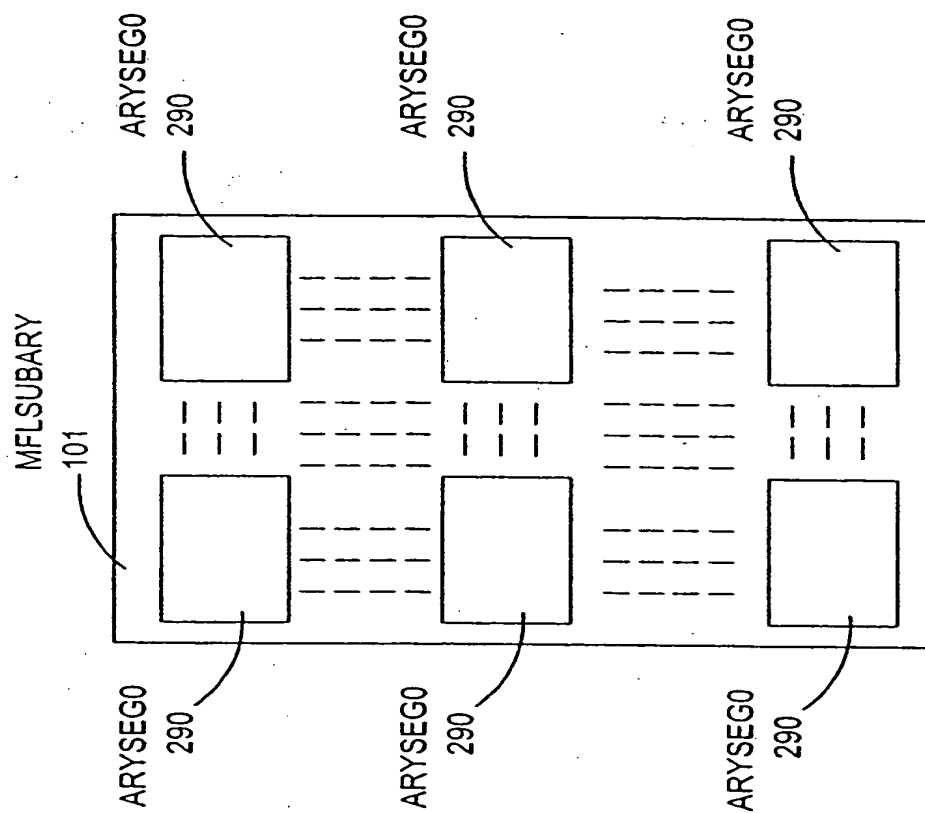


FIGURE 3C

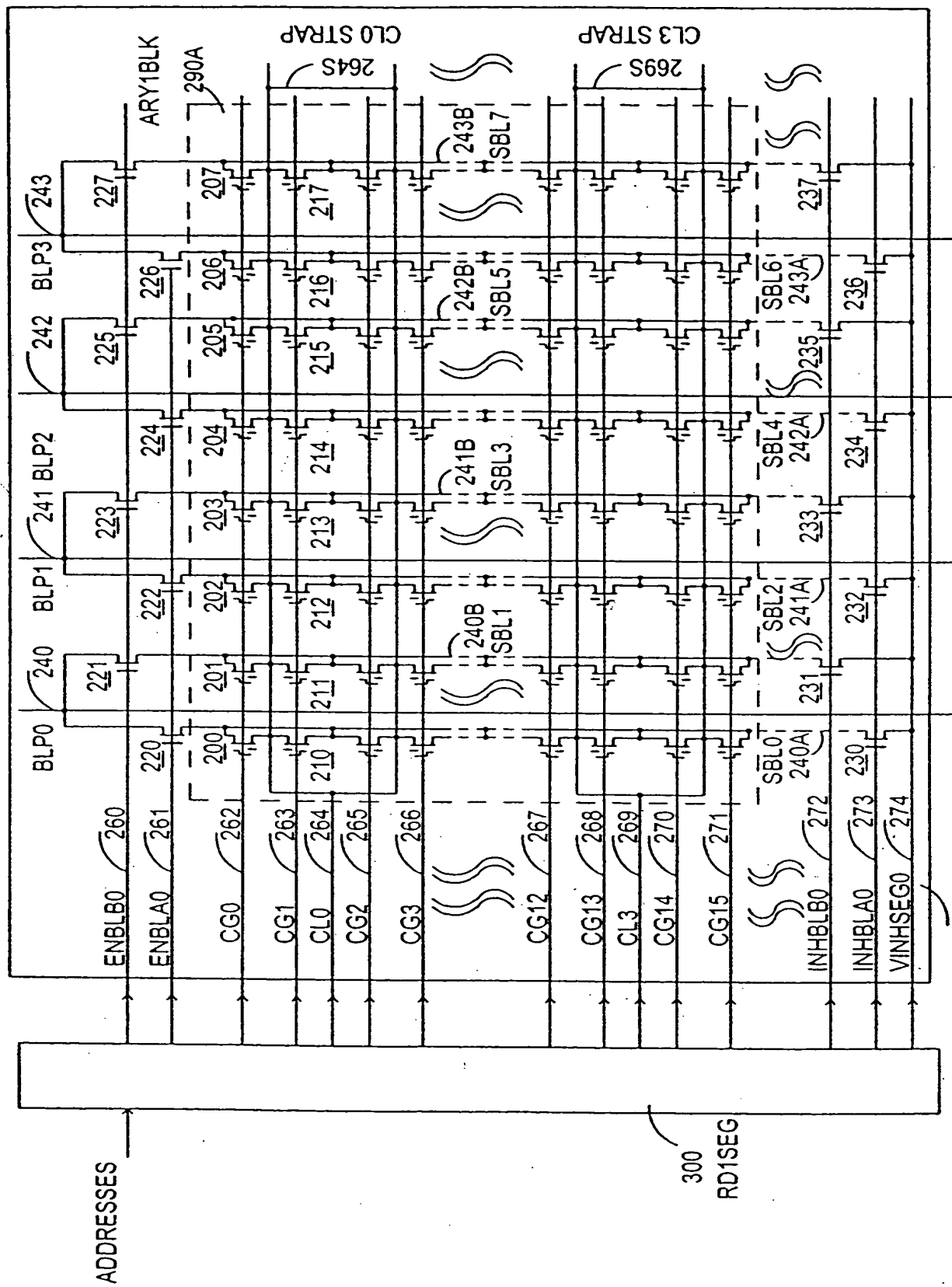


FIGURE 4A

290 ARYSEG0

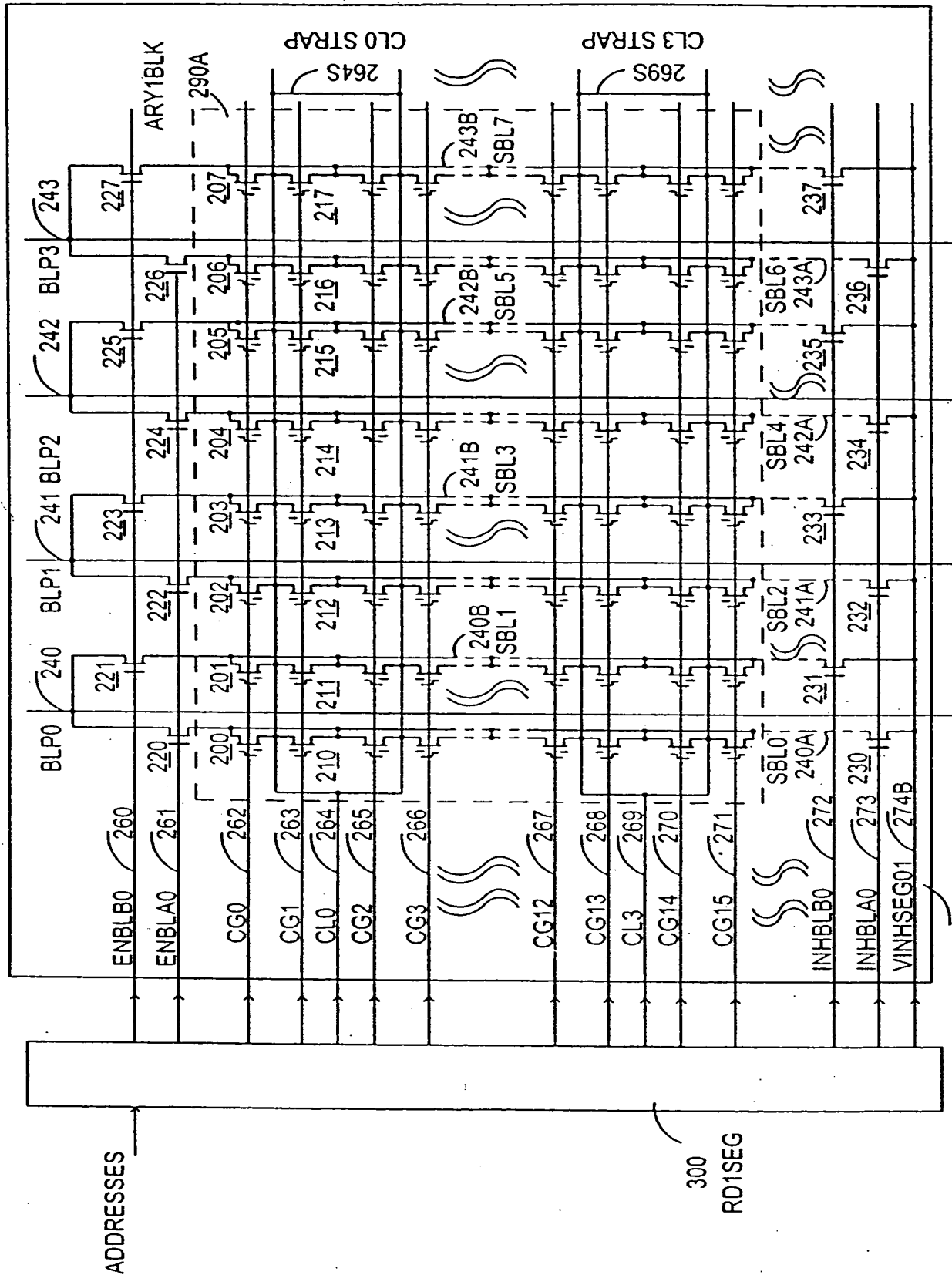


FIGURE 4B

290 ARYSEG0

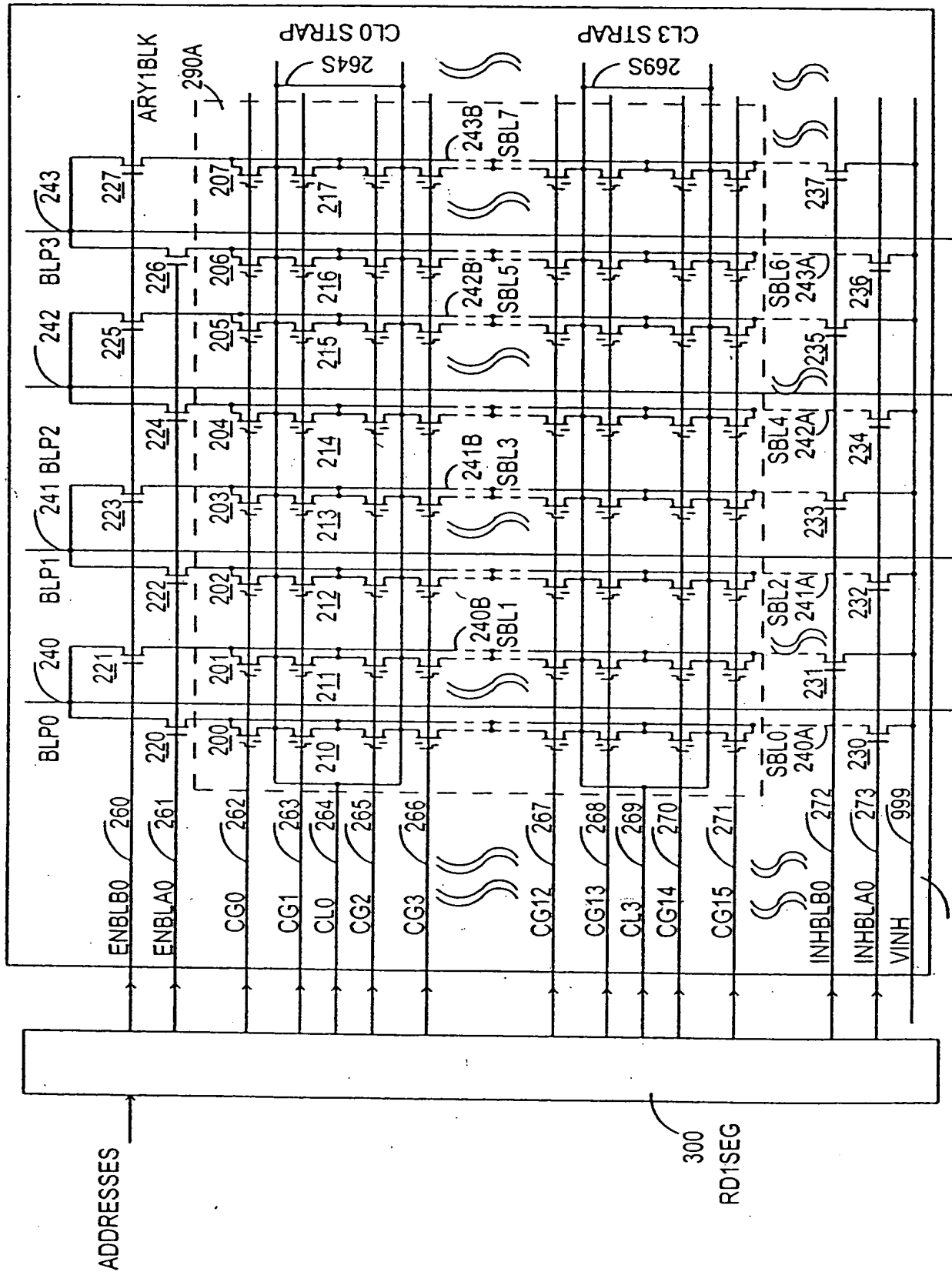


FIGURE 4C

290 ARYSEG0

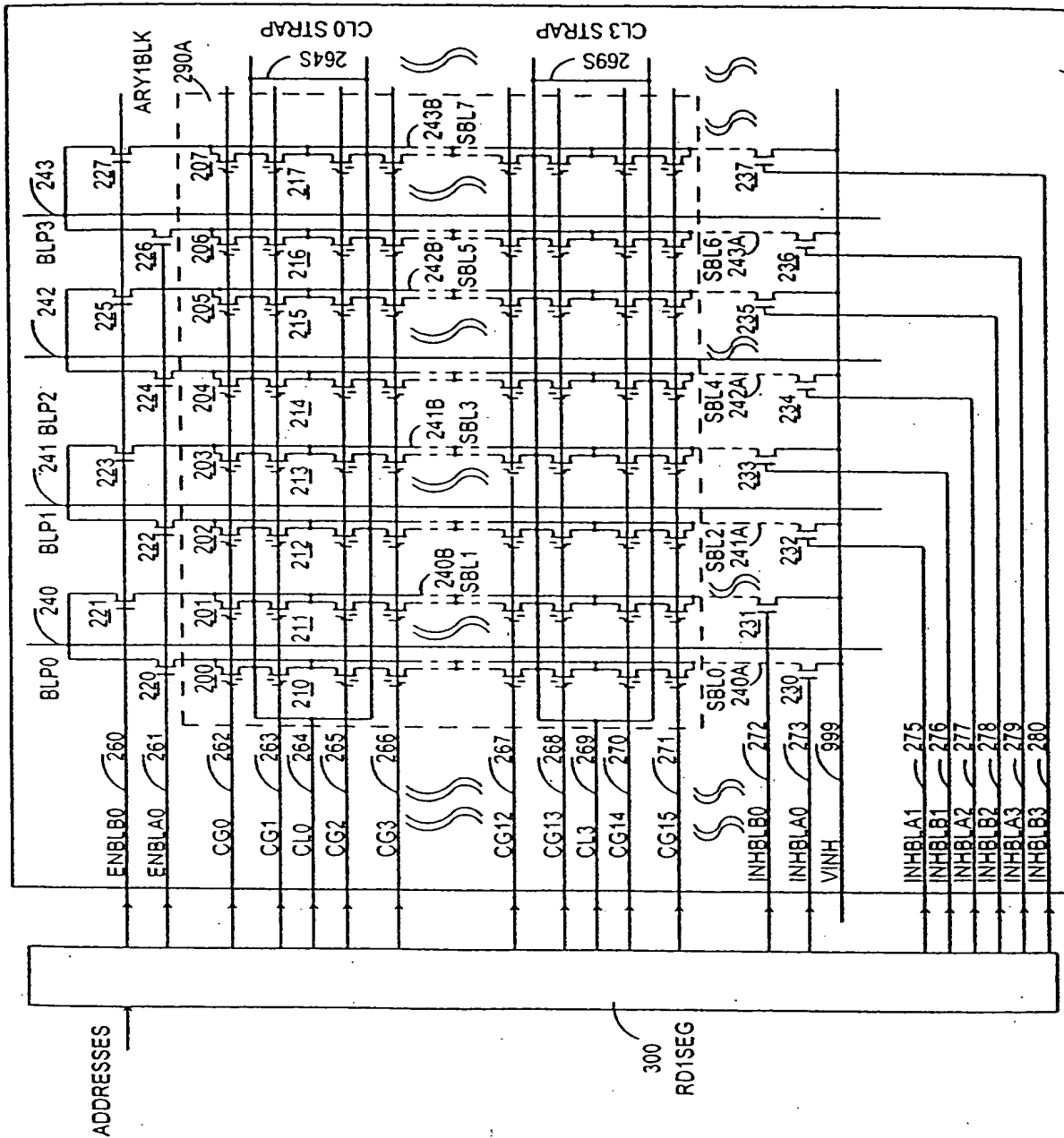


FIGURE 4D

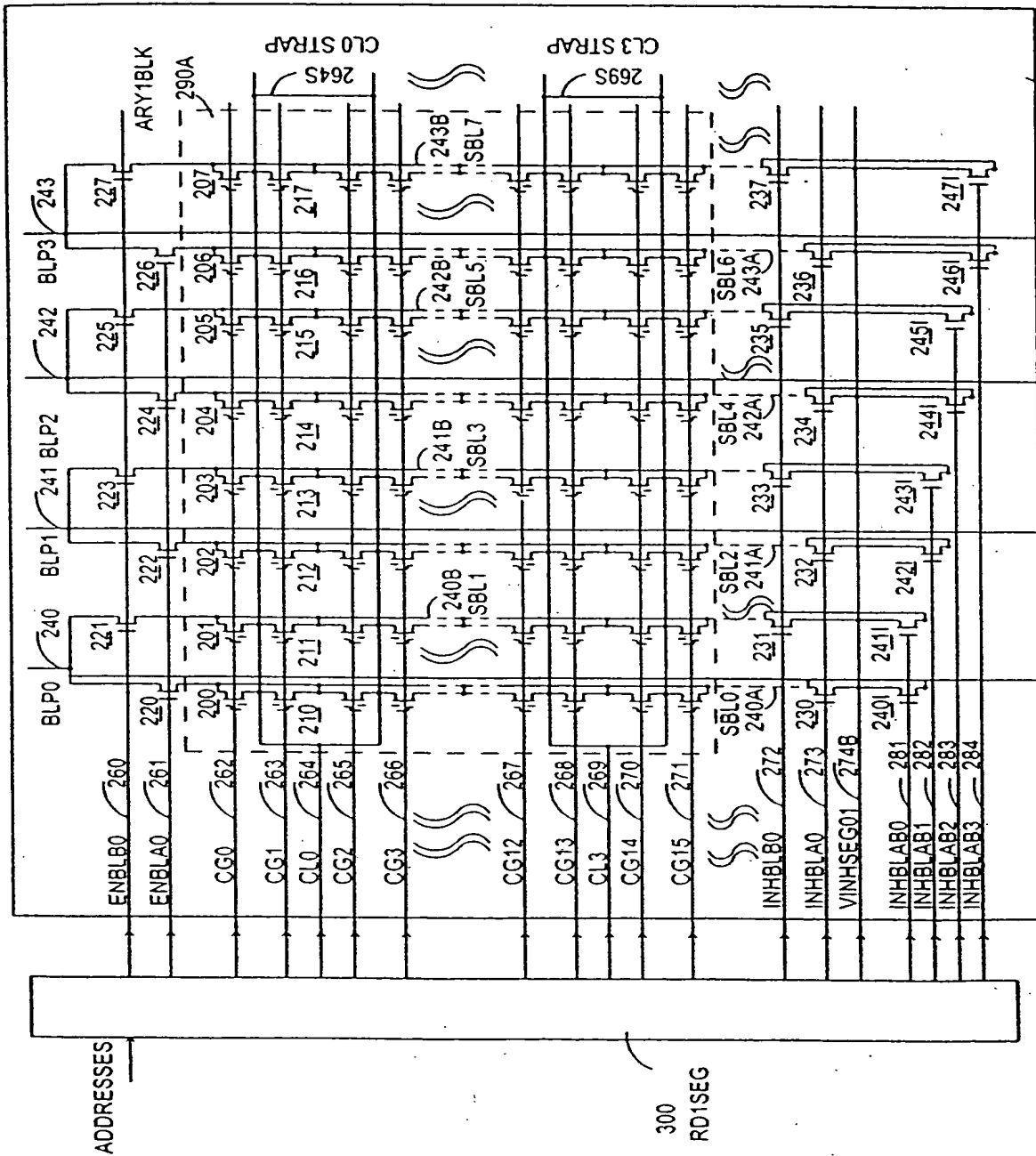


FIGURE 4E

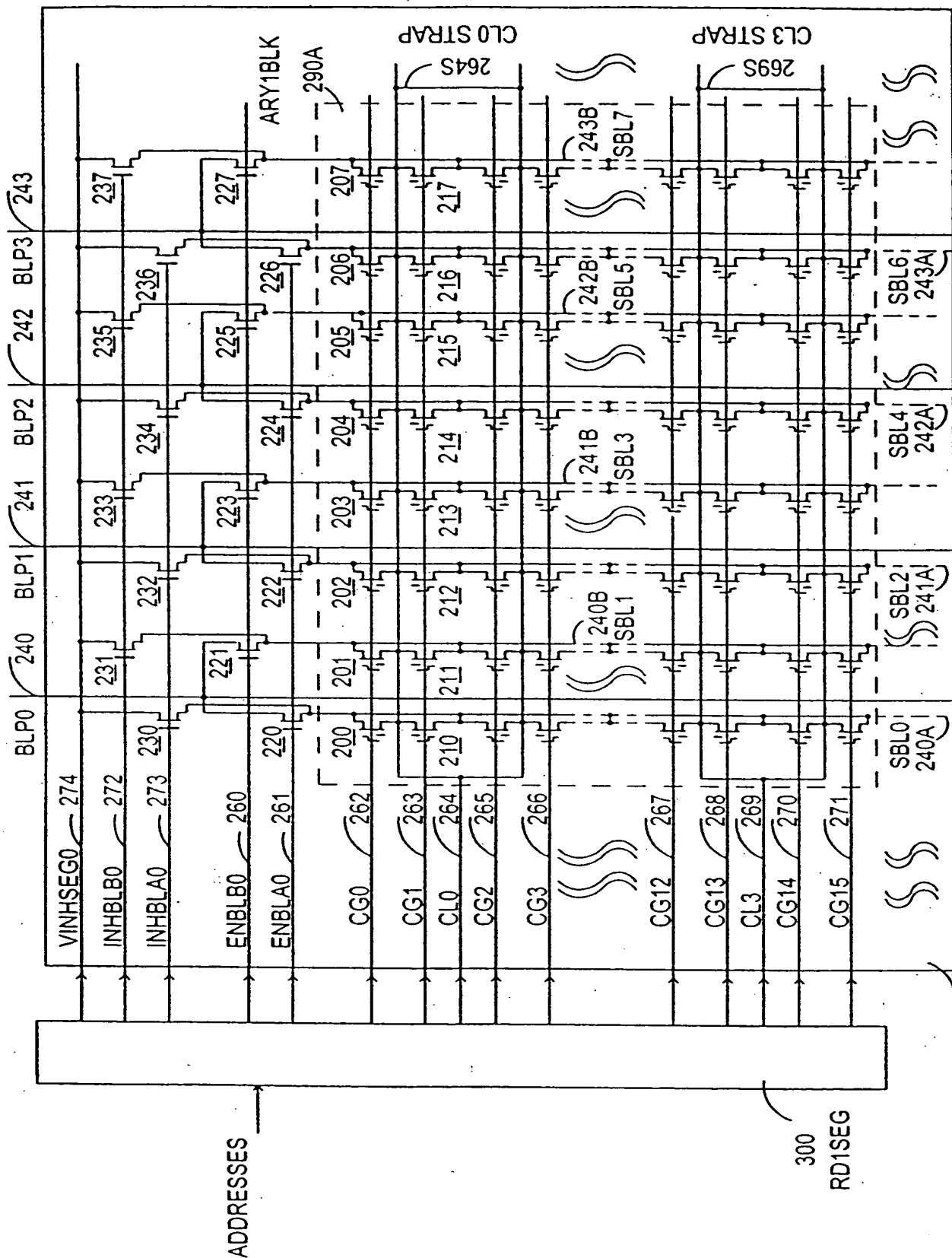


FIGURE 4F

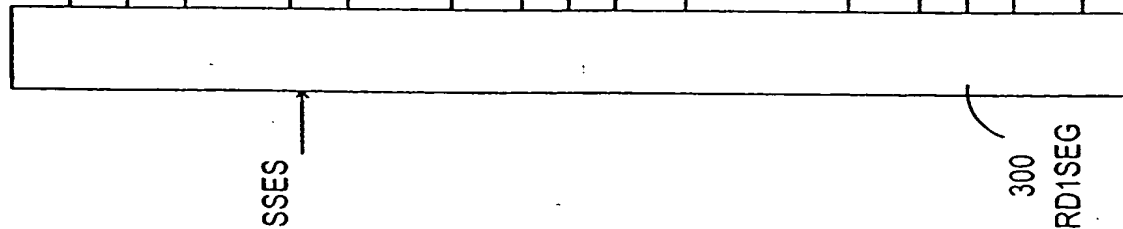


FIGURE 4F

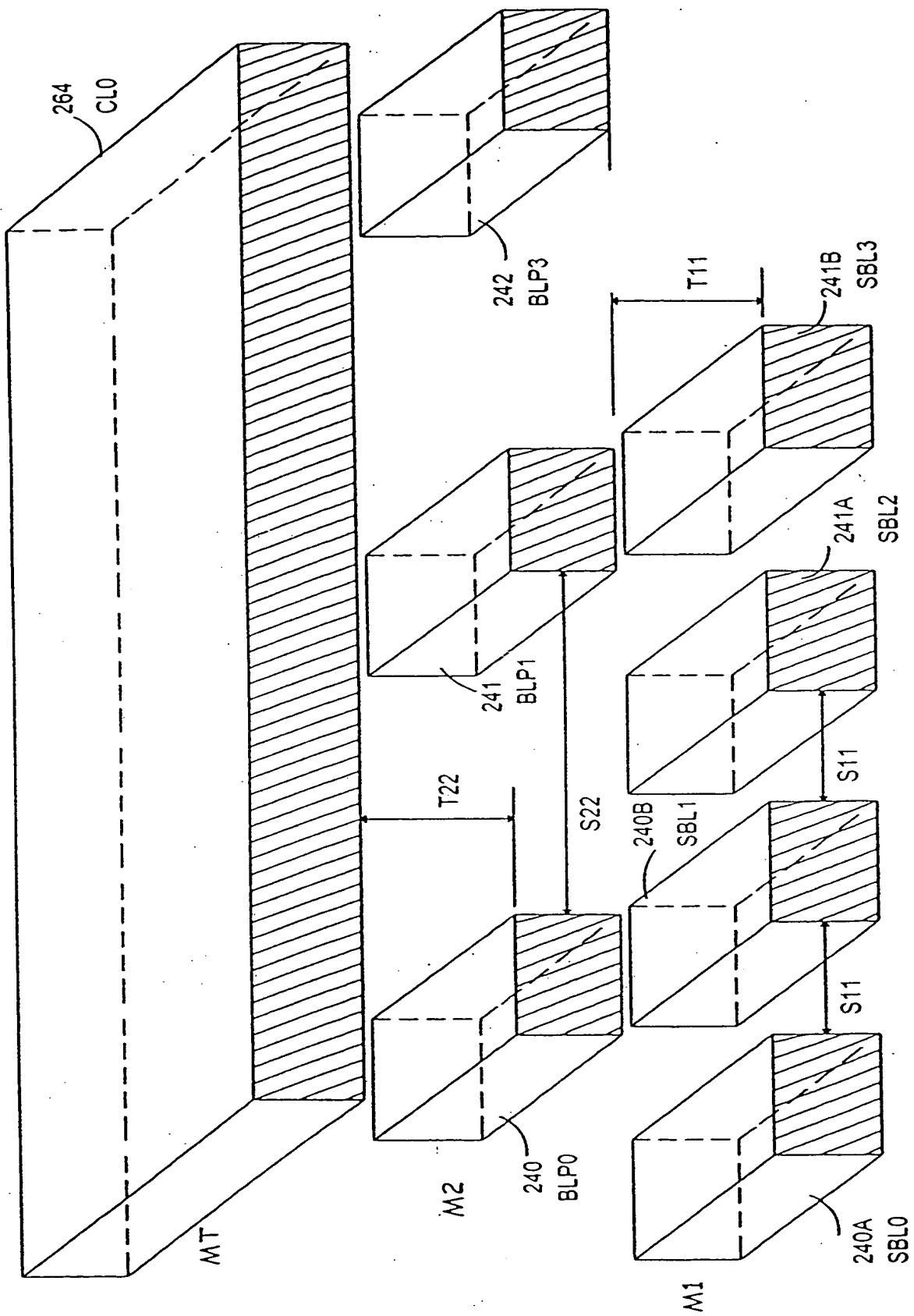


FIGURE 5A

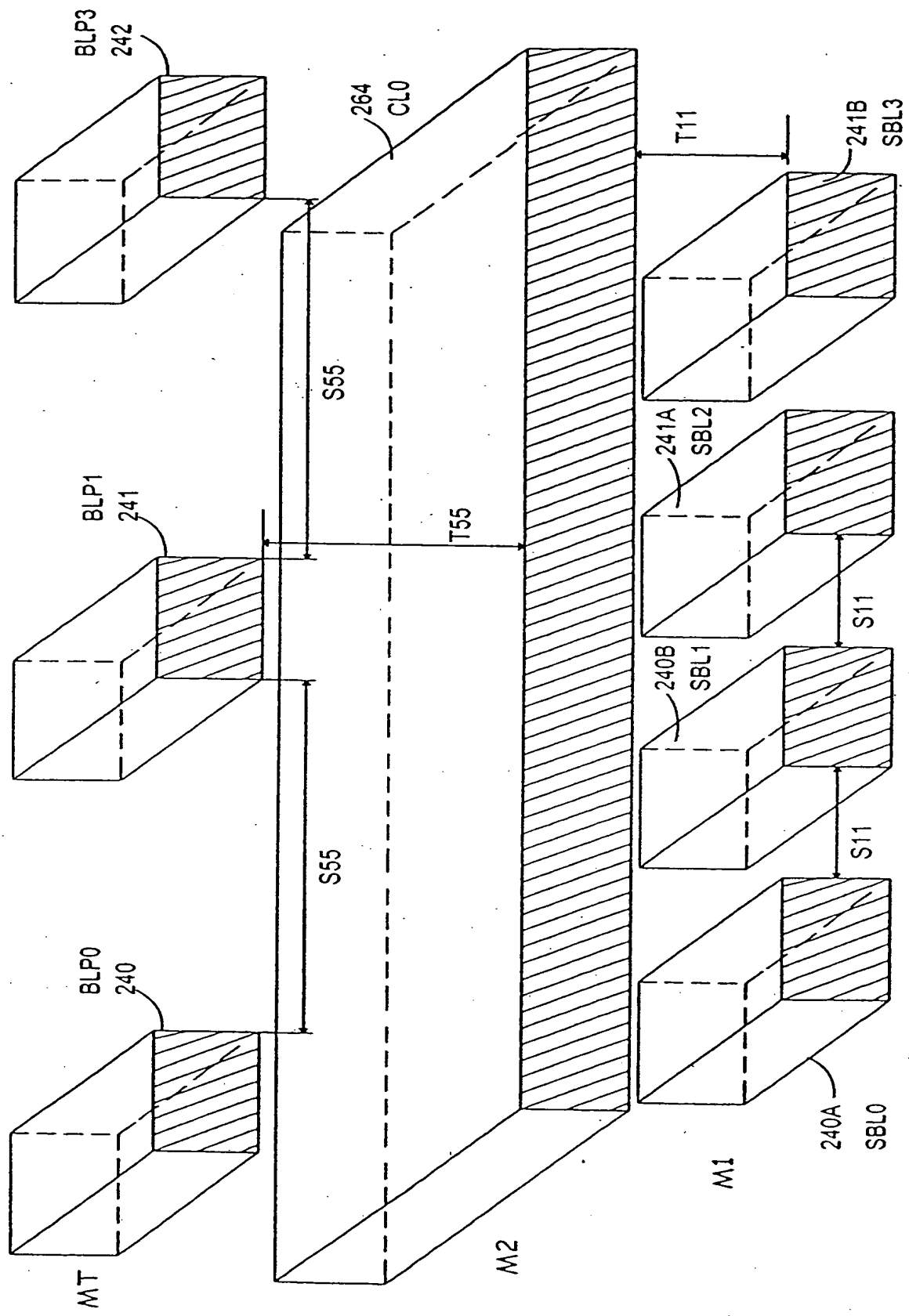
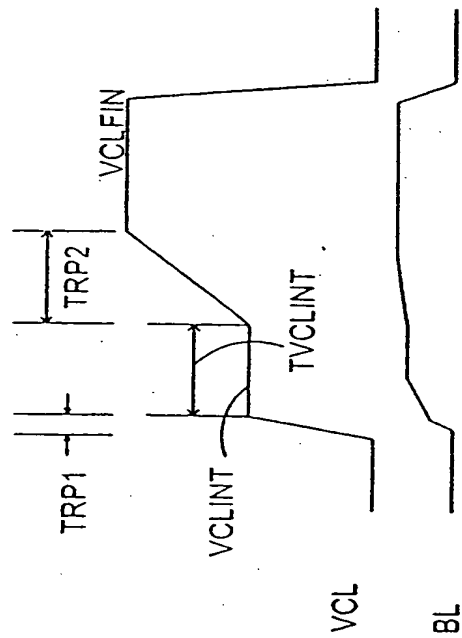
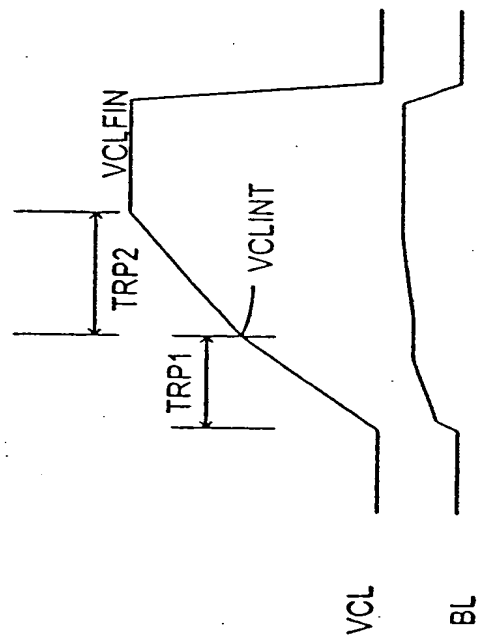


FIGURE 5B



2-STEP RAMP RATE CONTROL



FAST-SLOW RAMP RATE CONTROL

FIGURE 5C

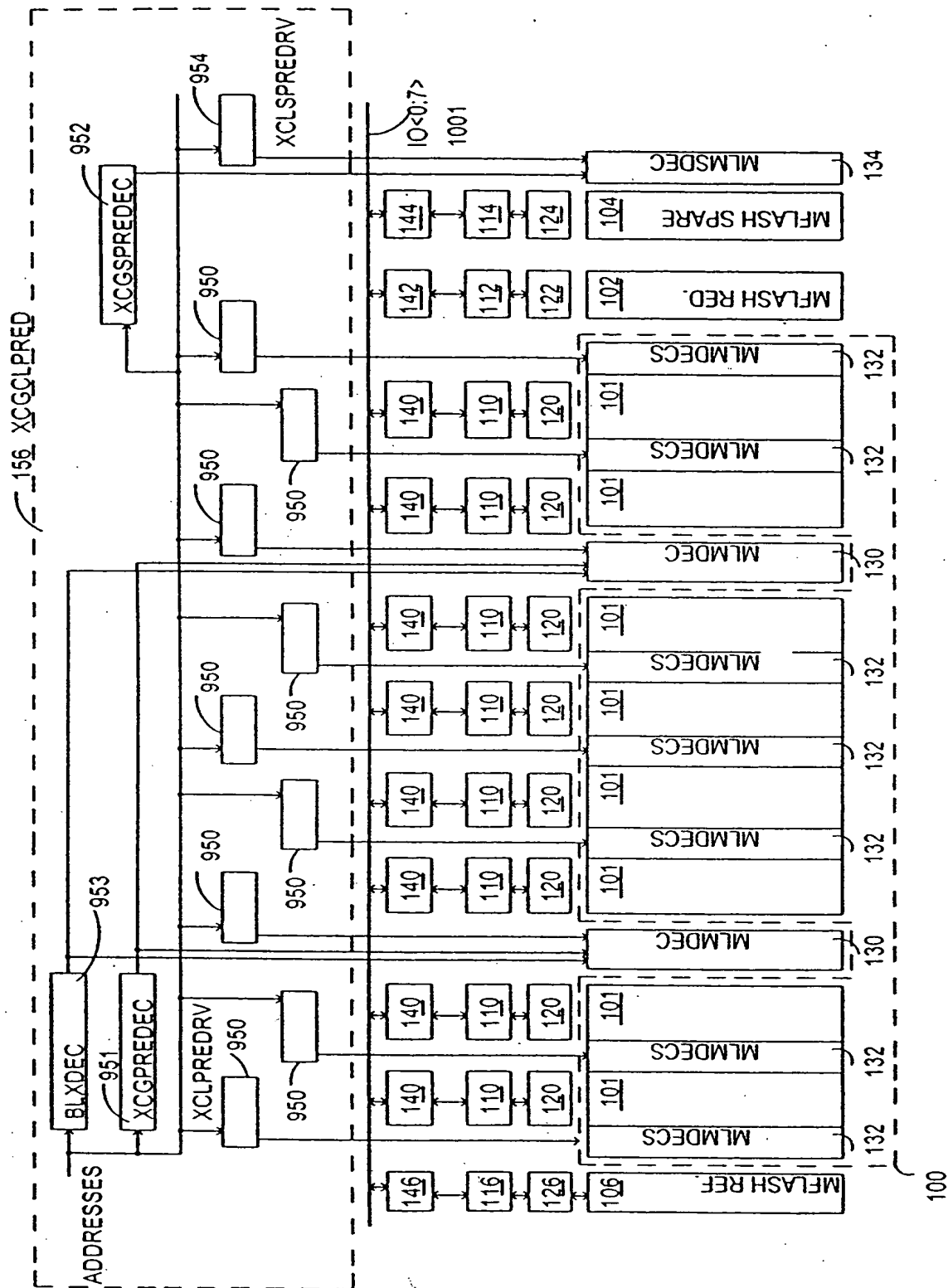


FIGURE 6

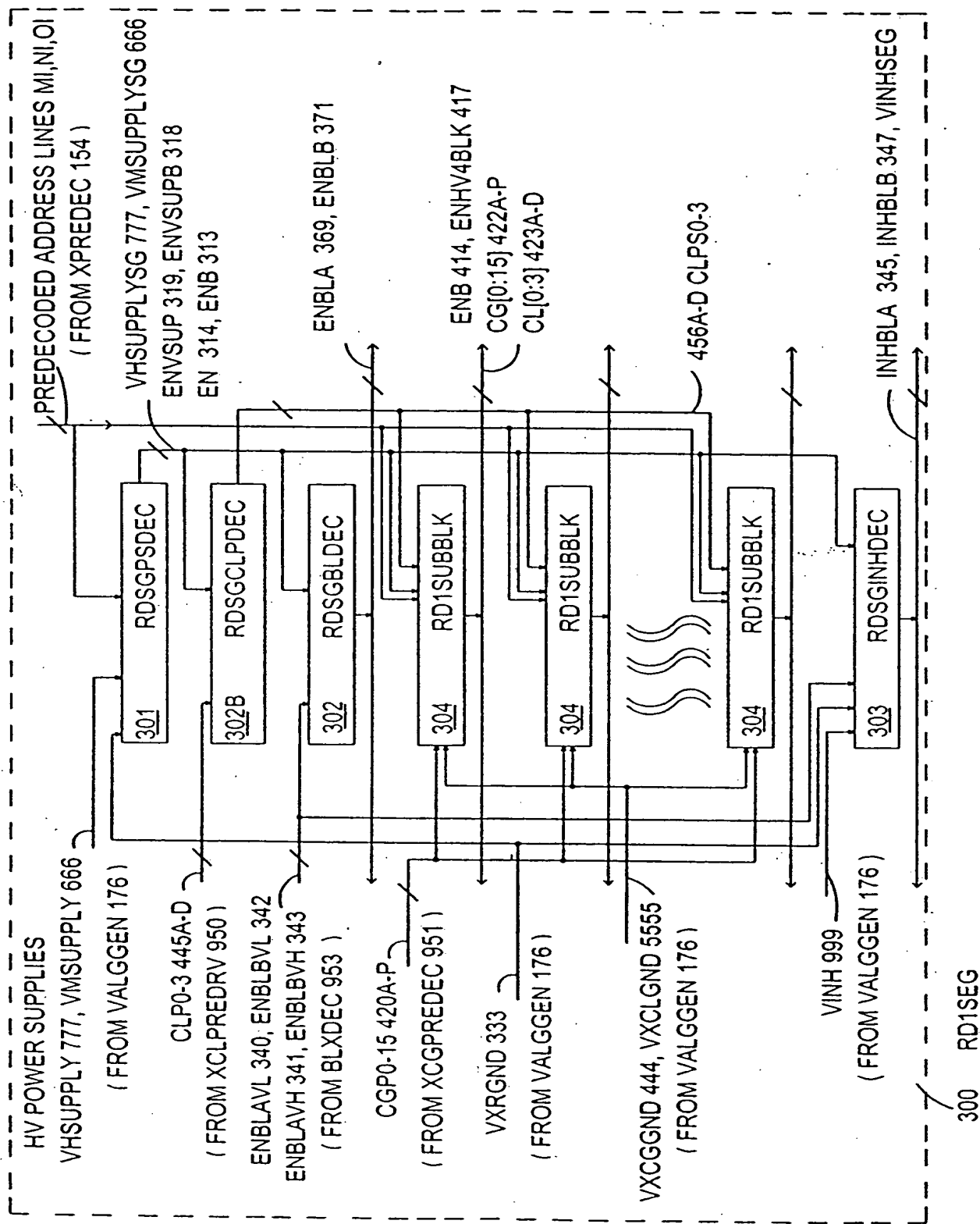


FIGURE 7

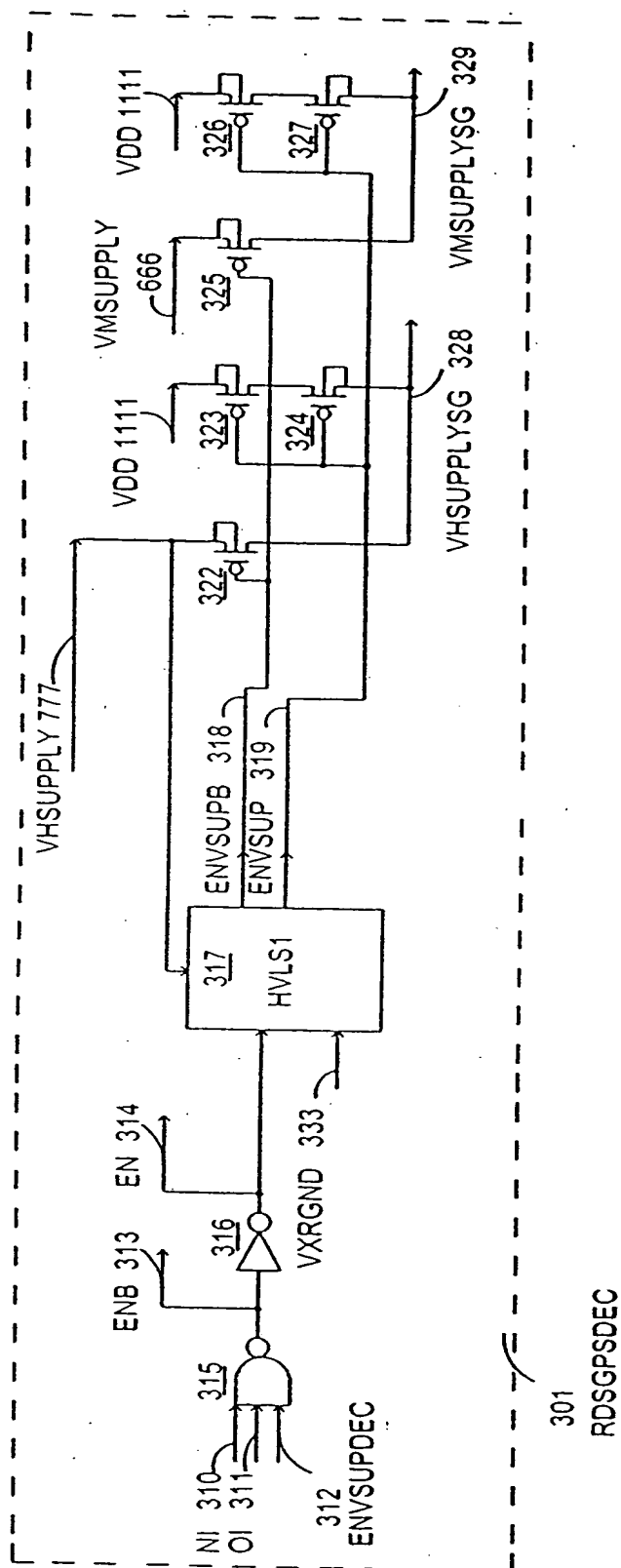


FIGURE 8

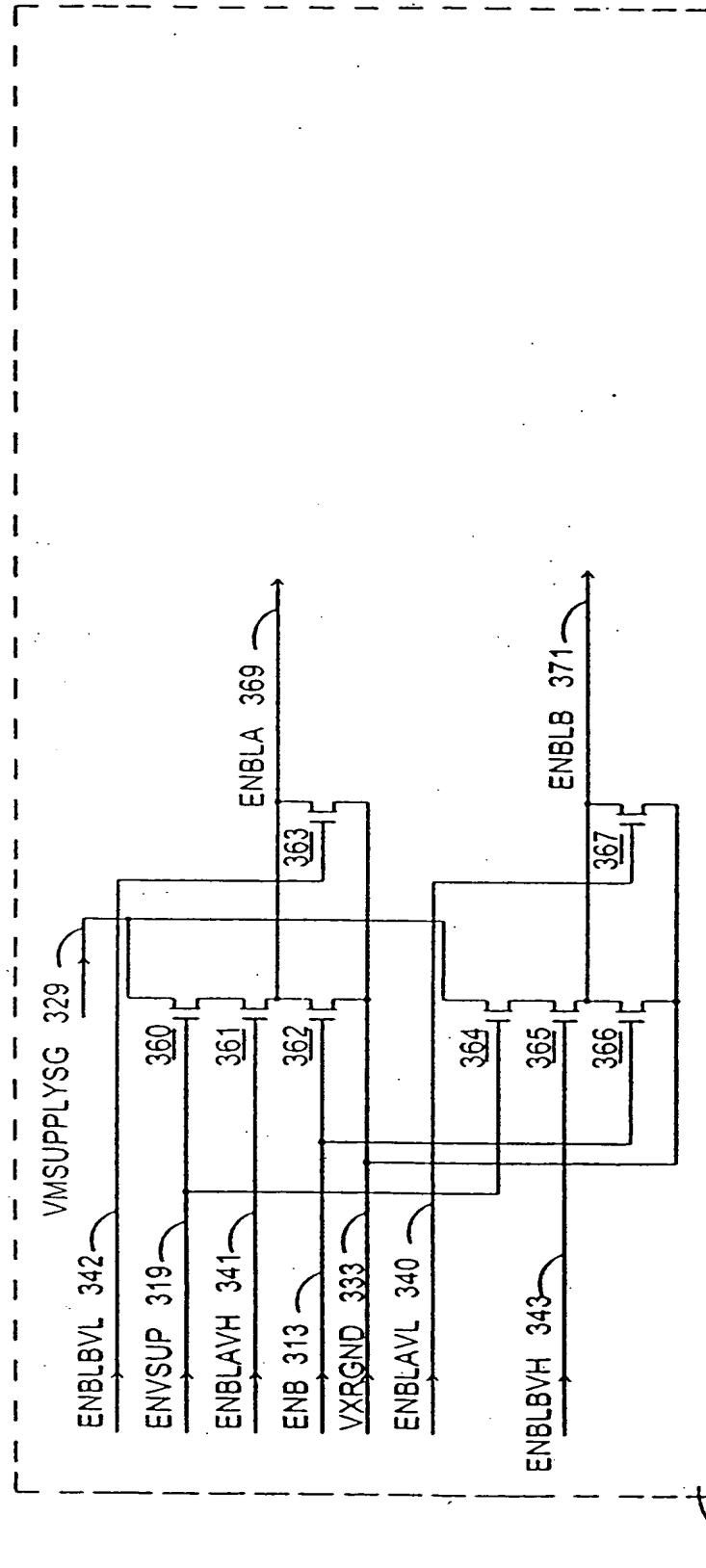
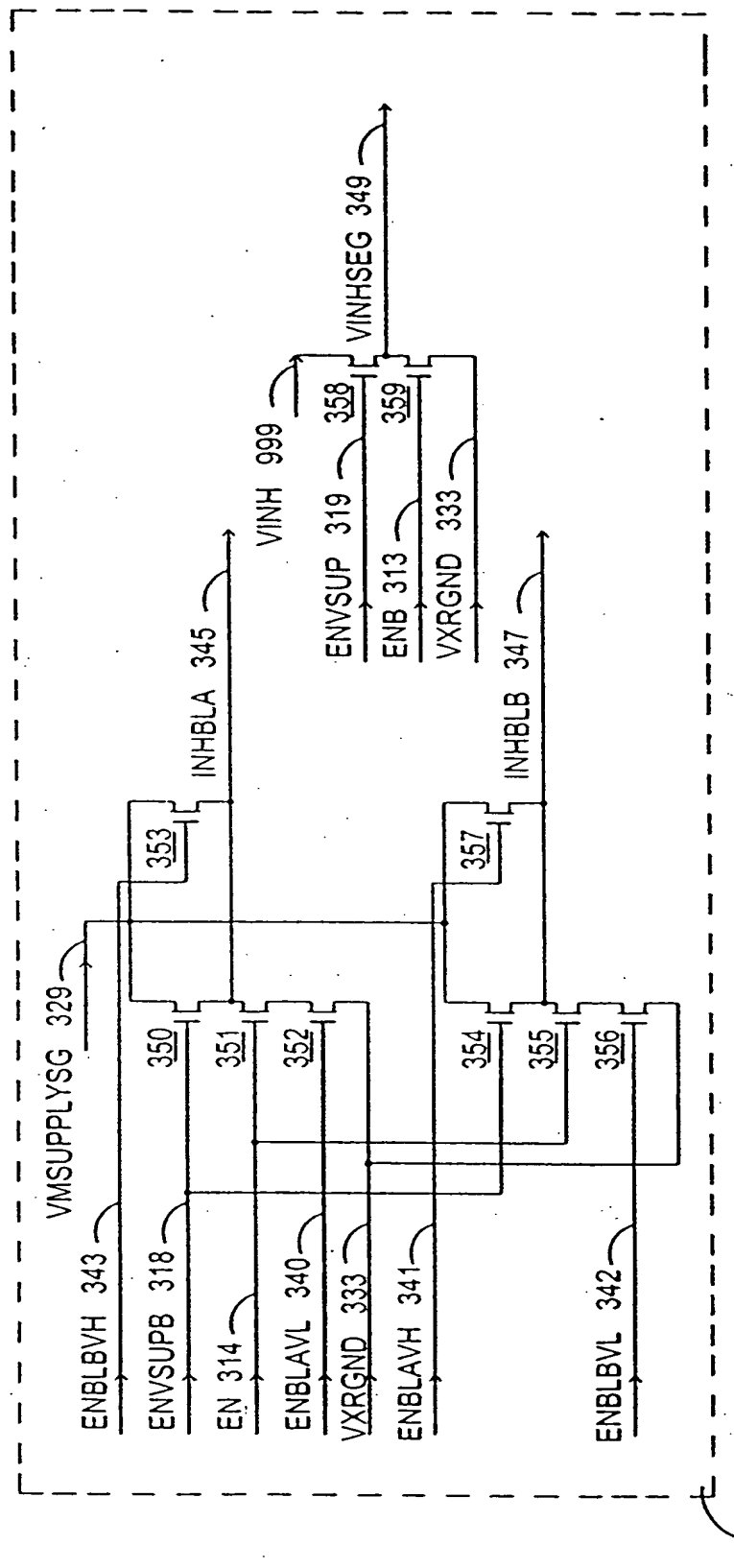


FIGURE 9A

302 RDSGBLDEC



303 RDGINHDEC

FIGURE 9B

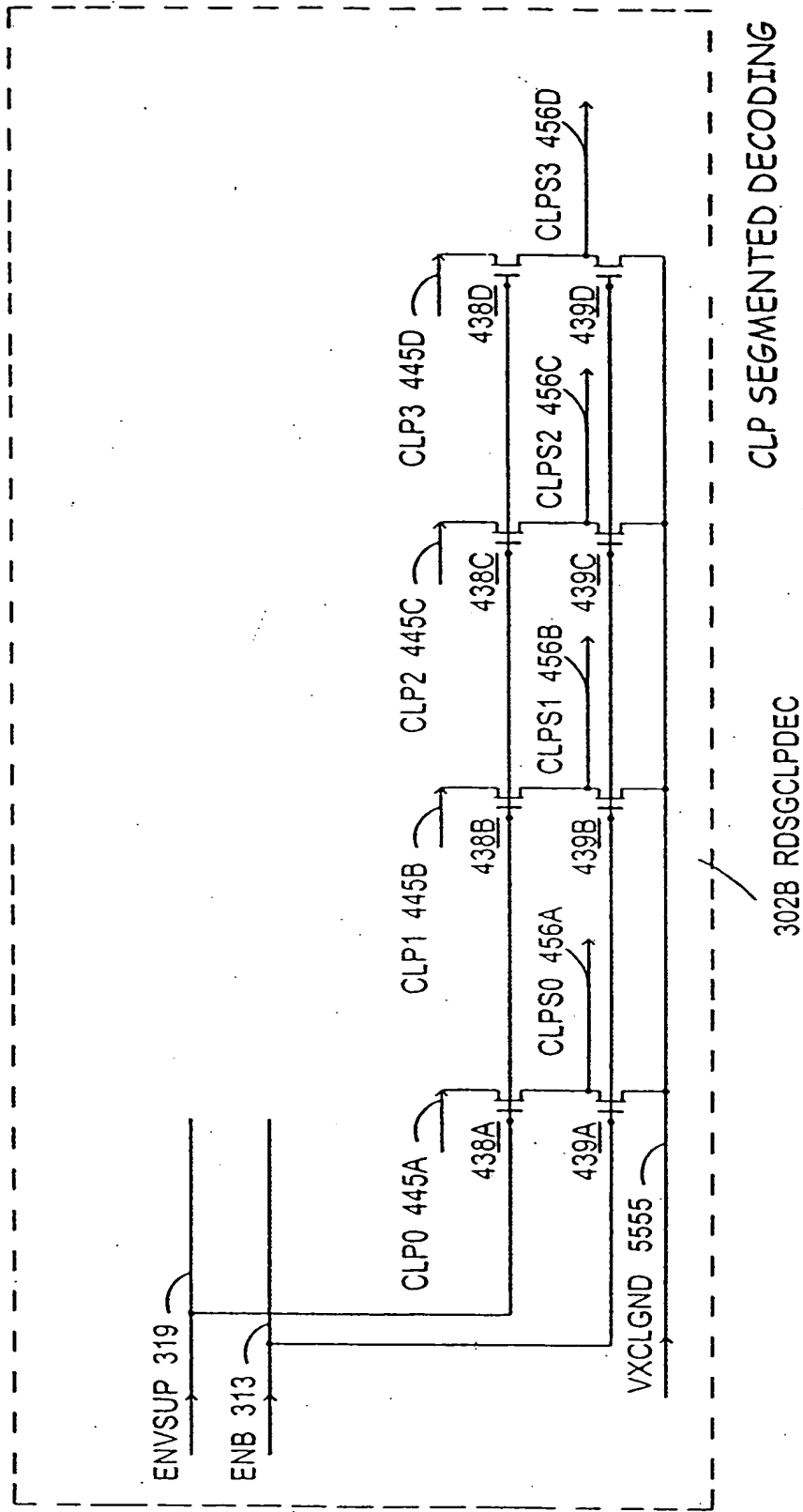


FIGURE 9C

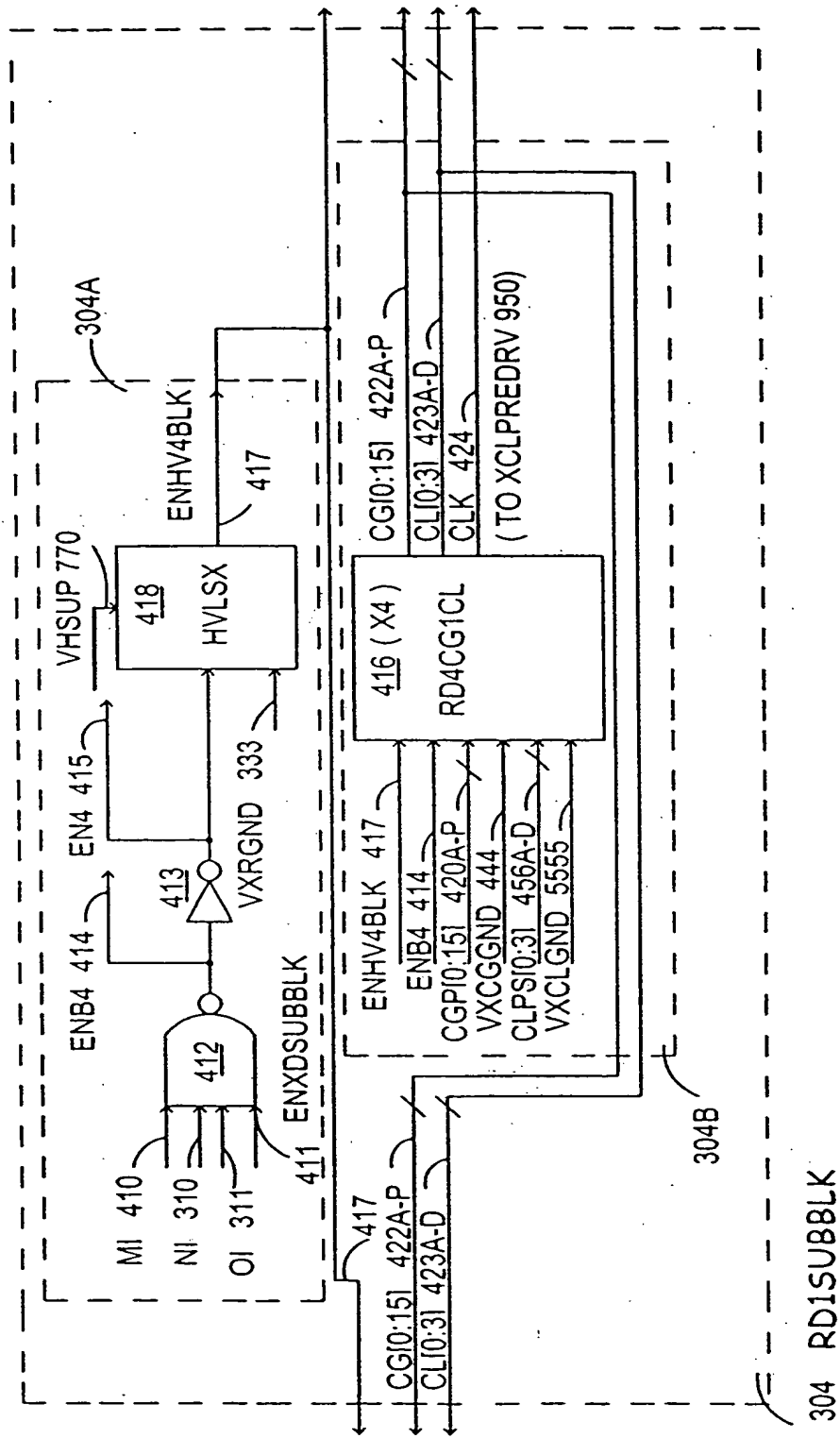


FIGURE 10

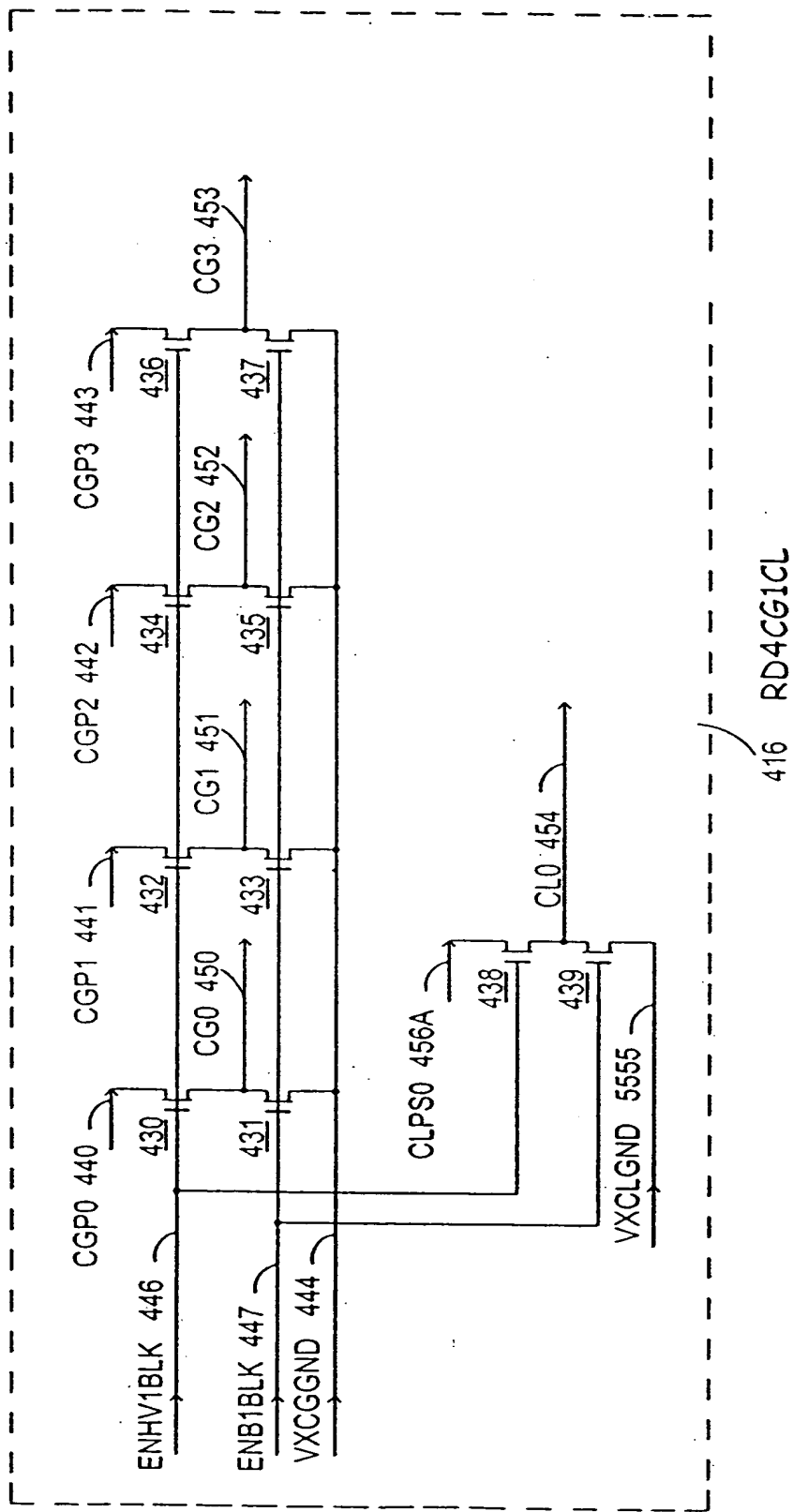


FIGURE 11A

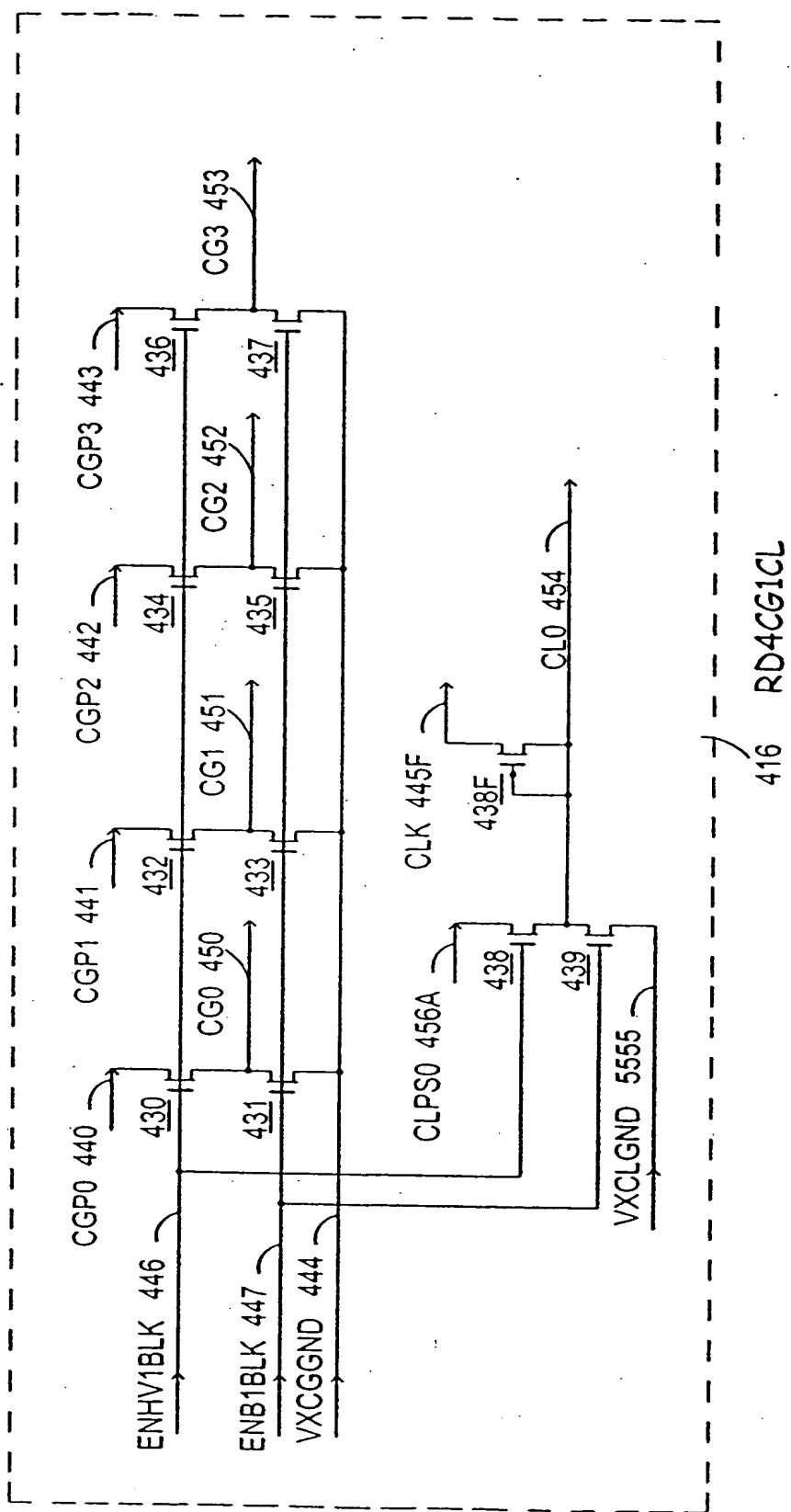


FIGURE 11B

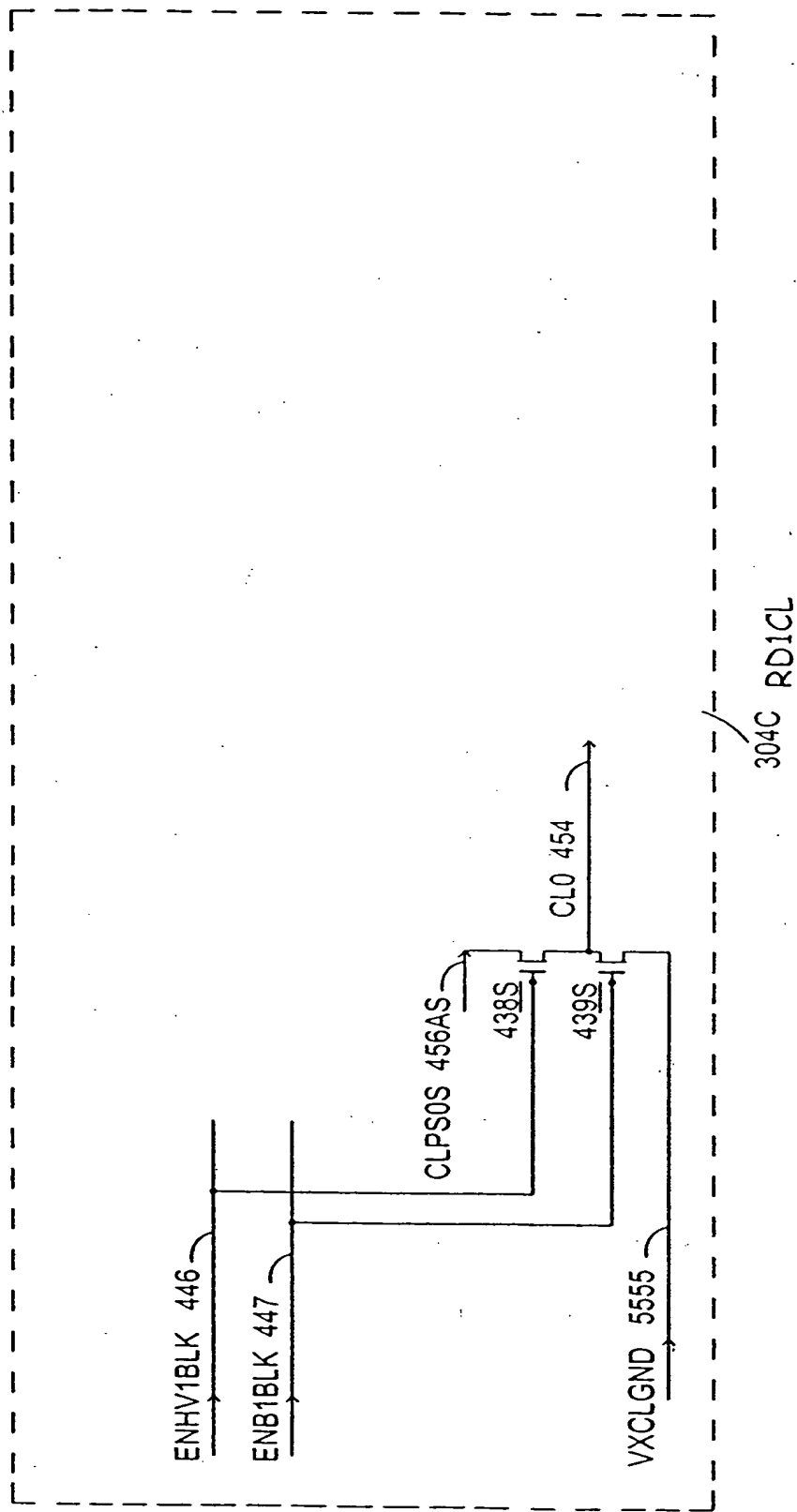


FIGURE 11C

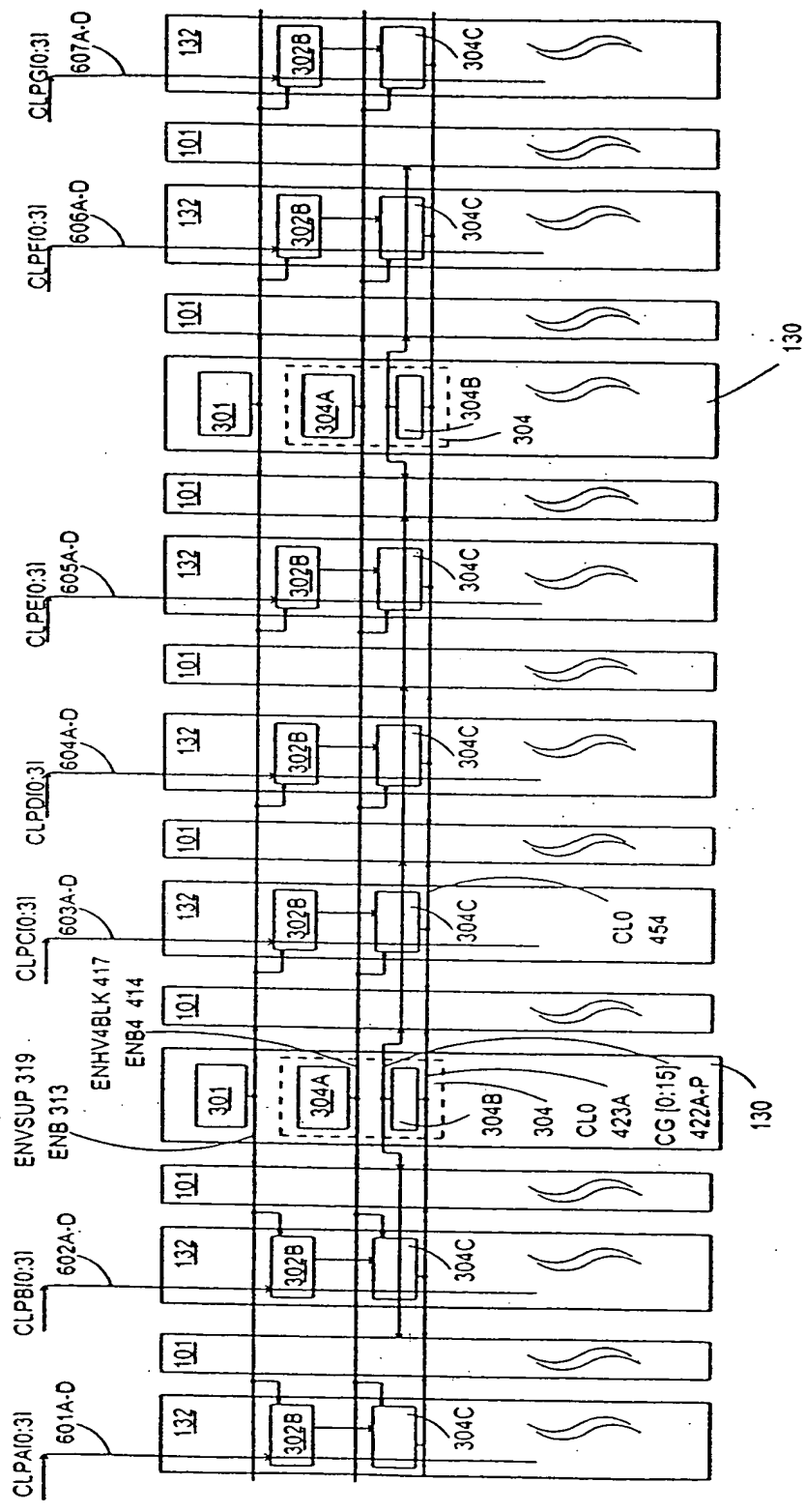


FIGURE 12

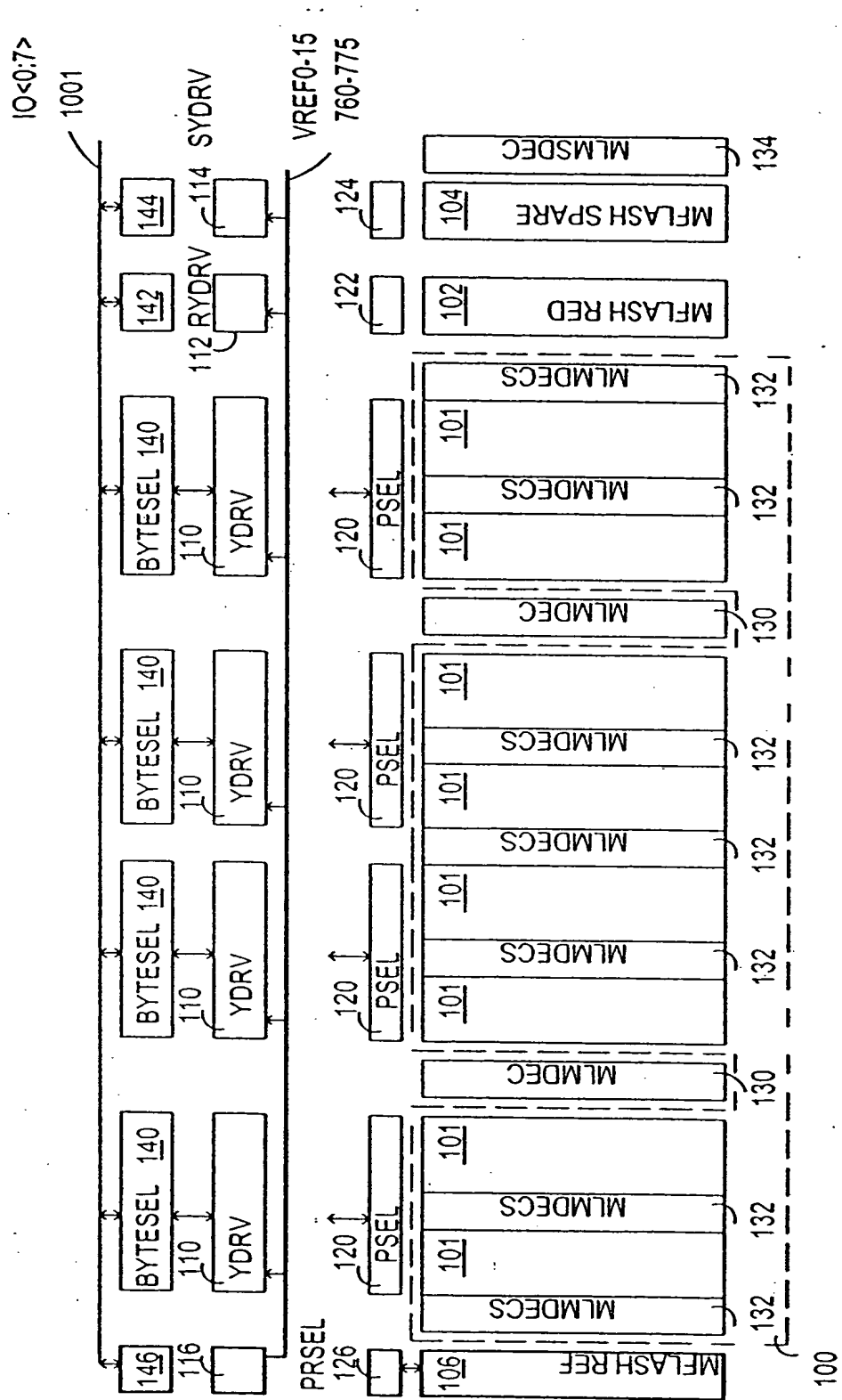


FIGURE 13

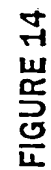


FIGURE 14

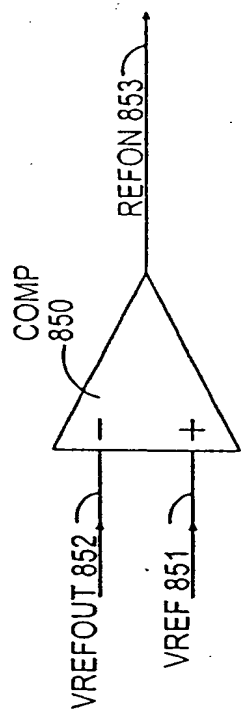


FIGURE 15

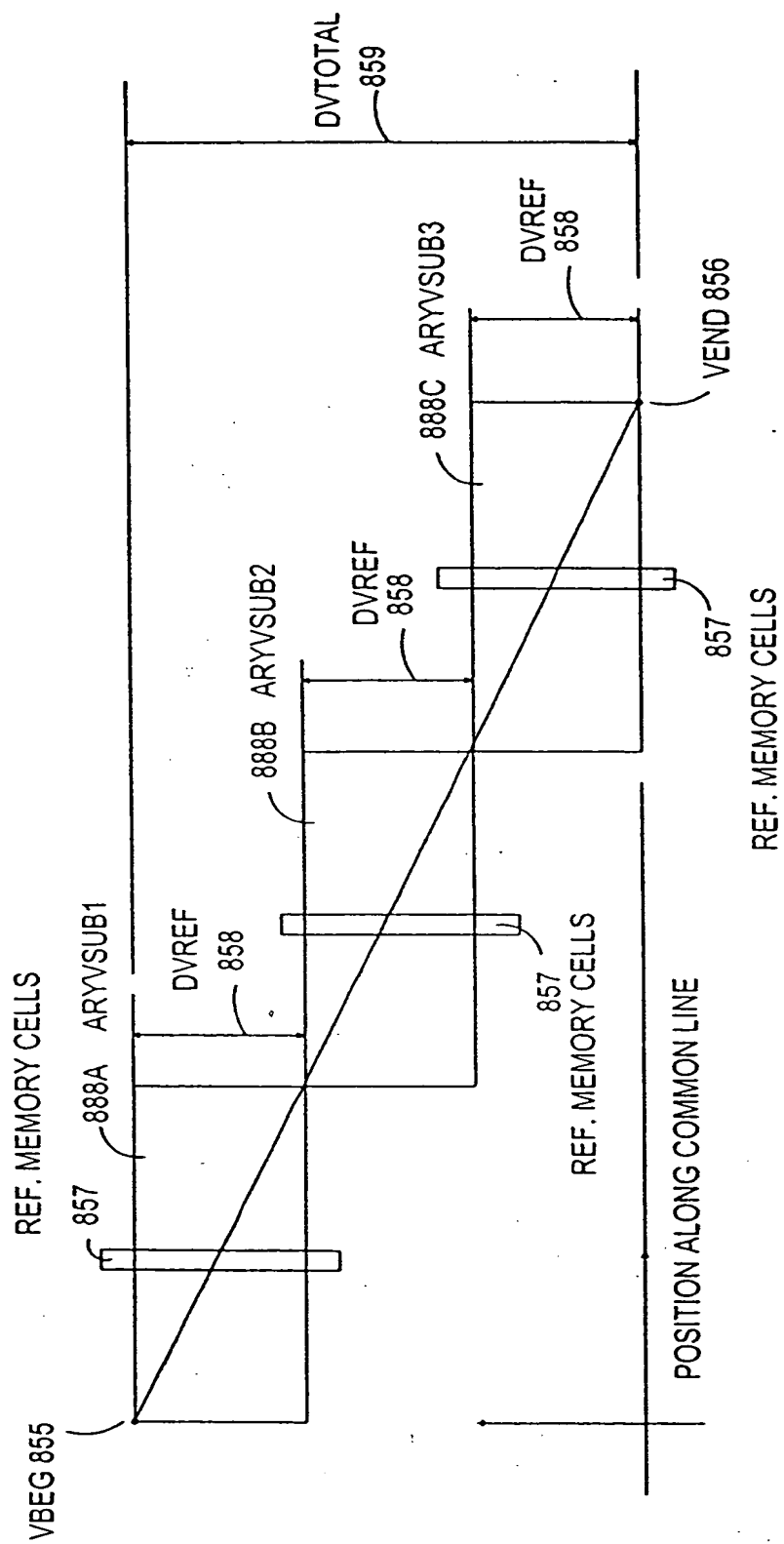


FIGURE 16

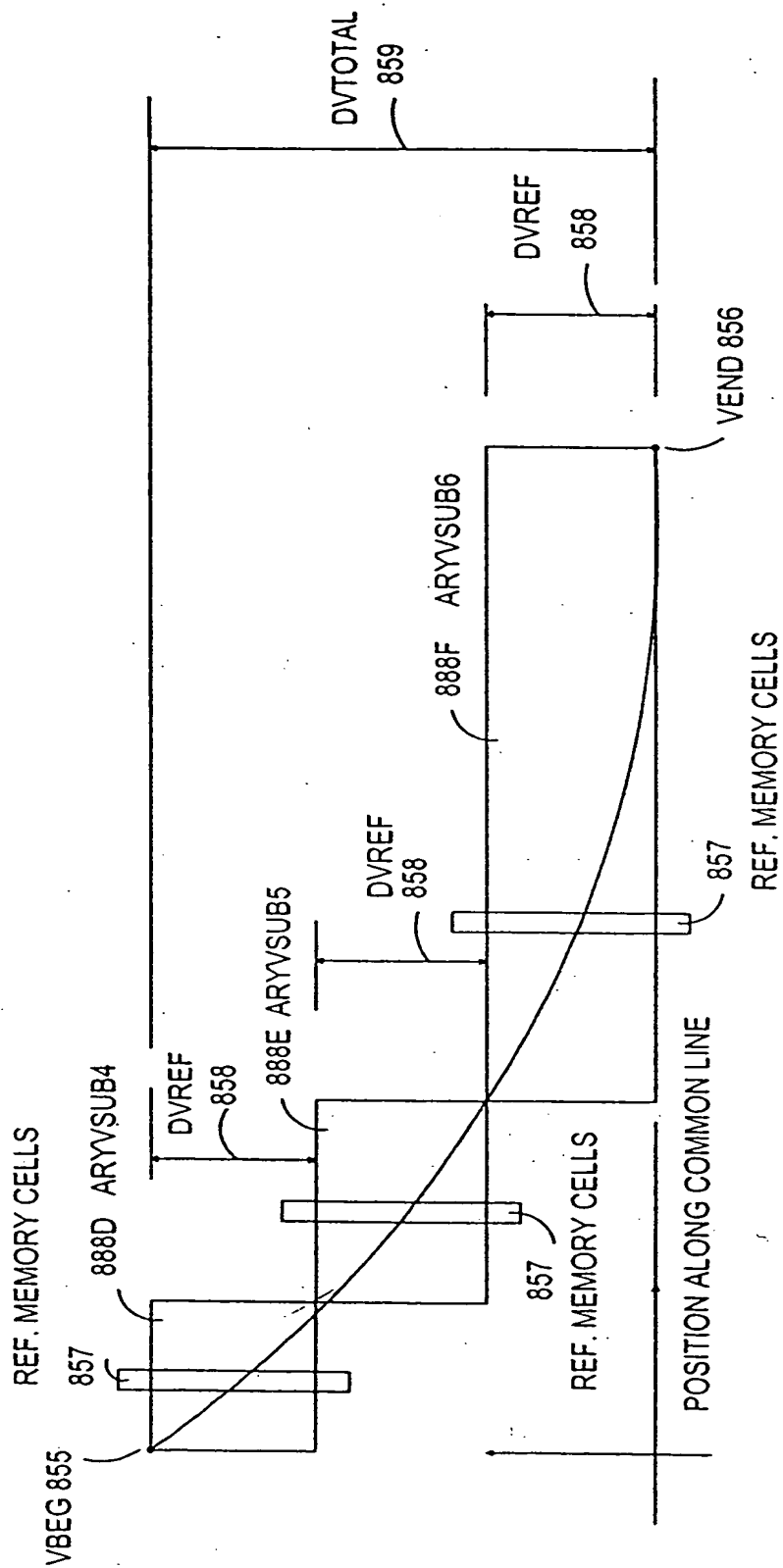


FIGURE 17

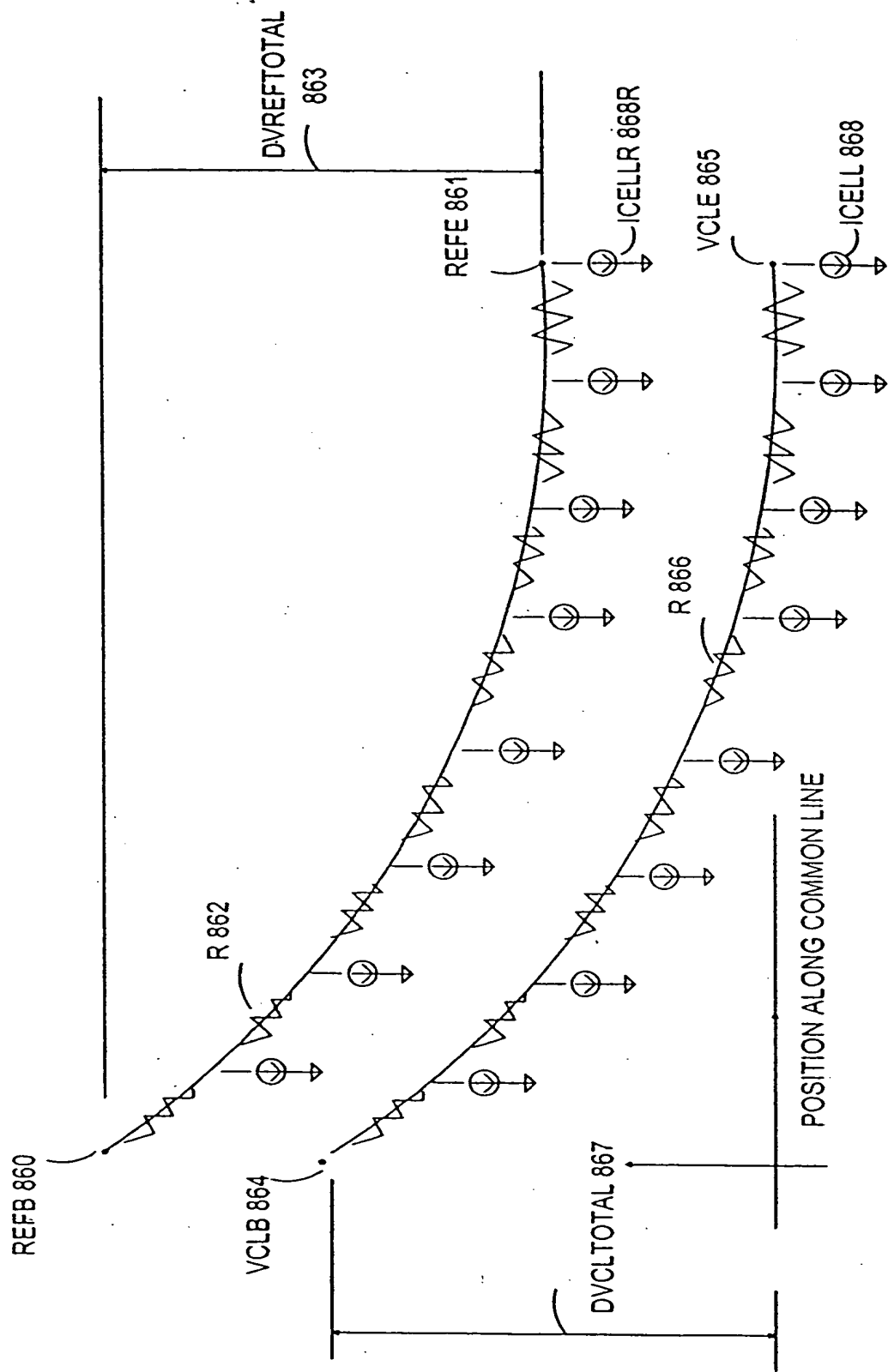


FIGURE 18

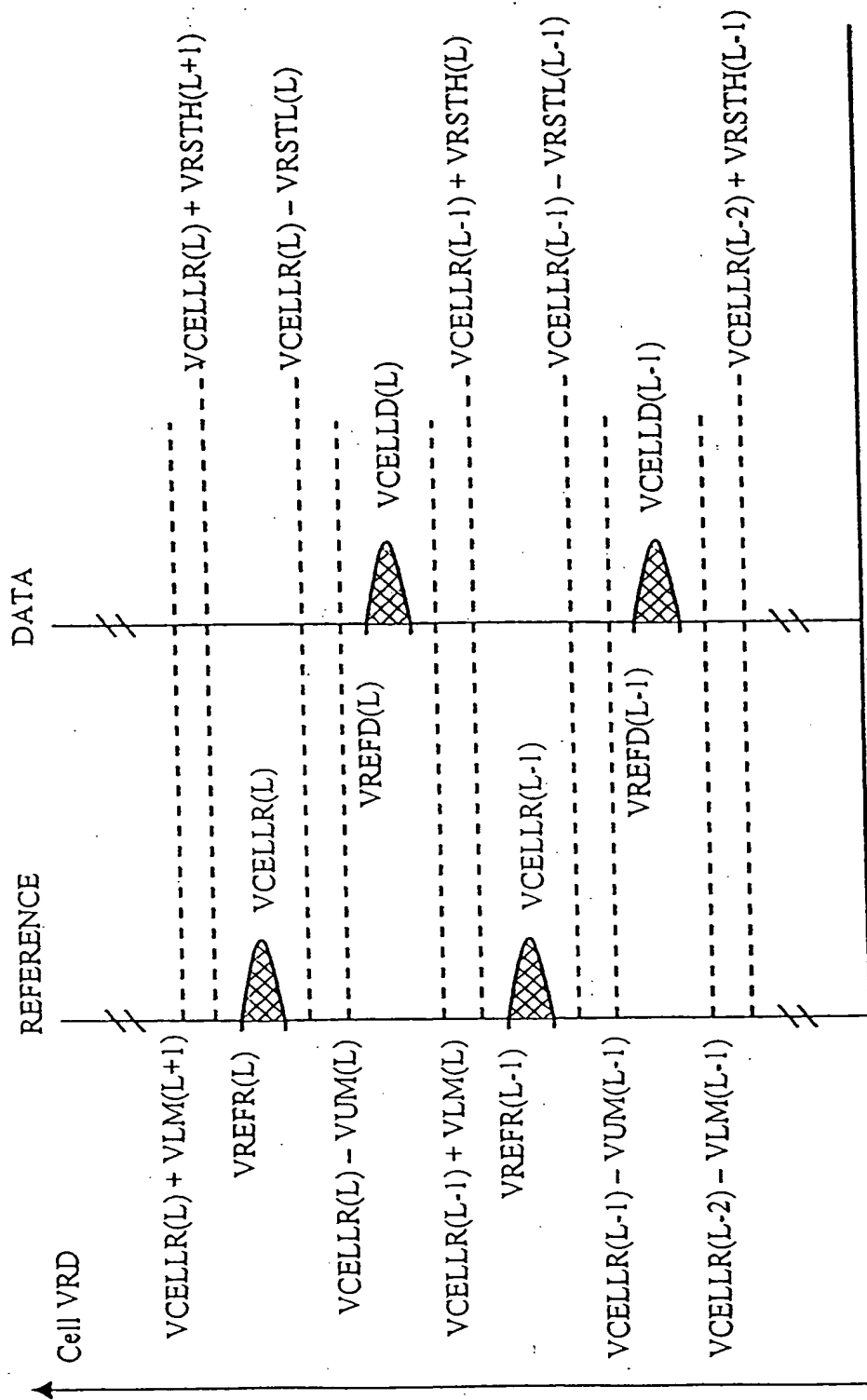


FIG. 19A

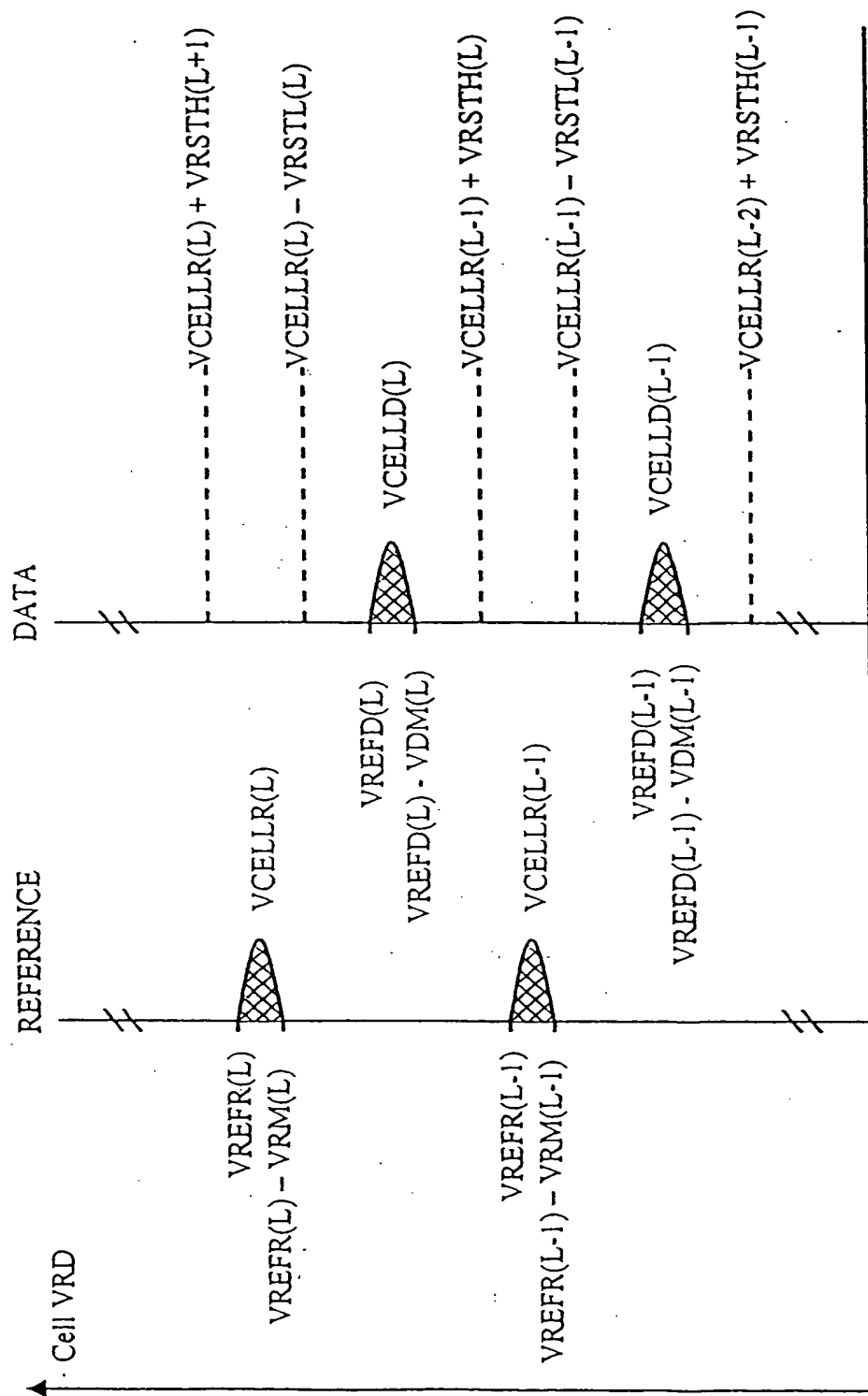


FIG. 19B

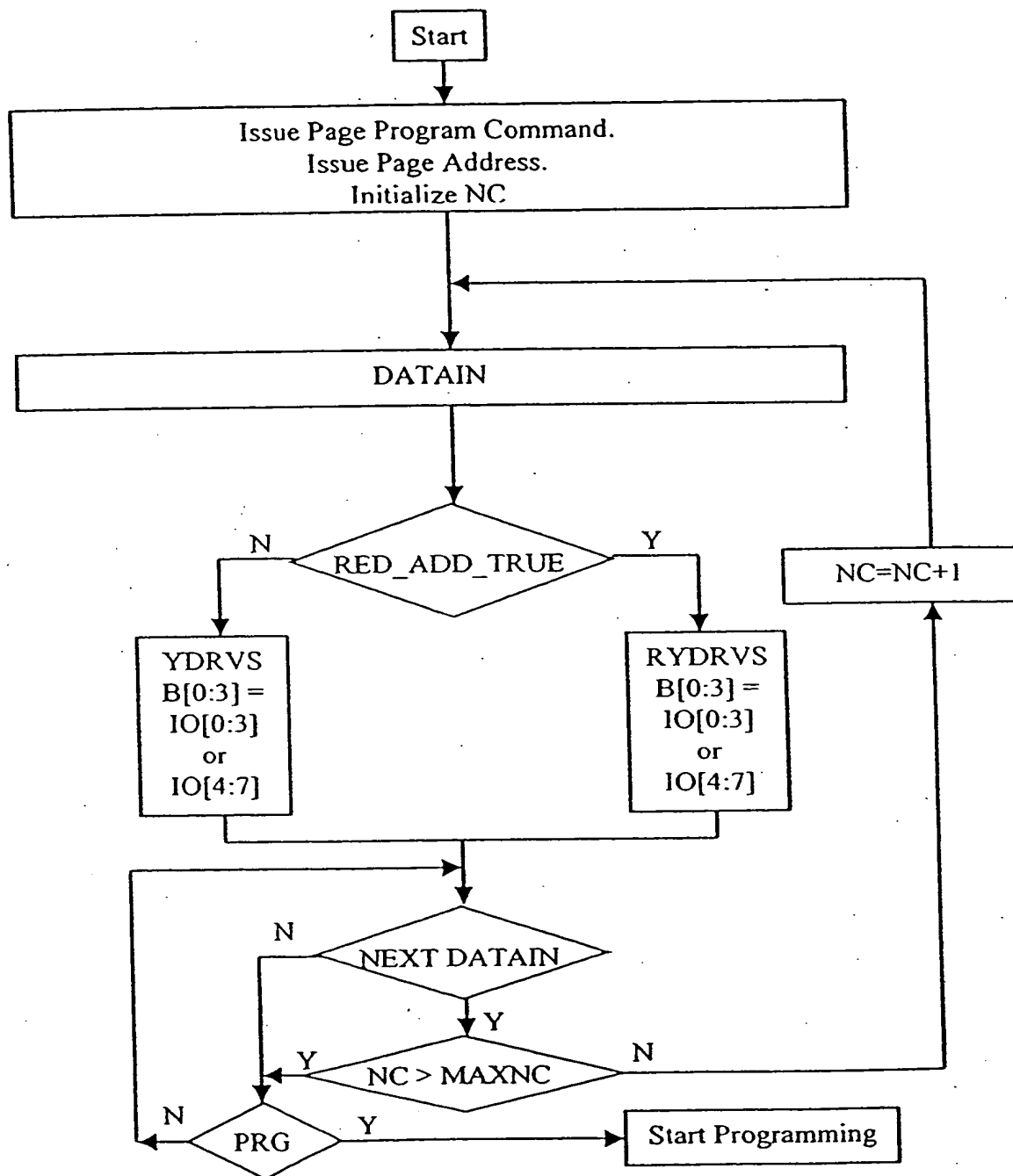


FIG. 20

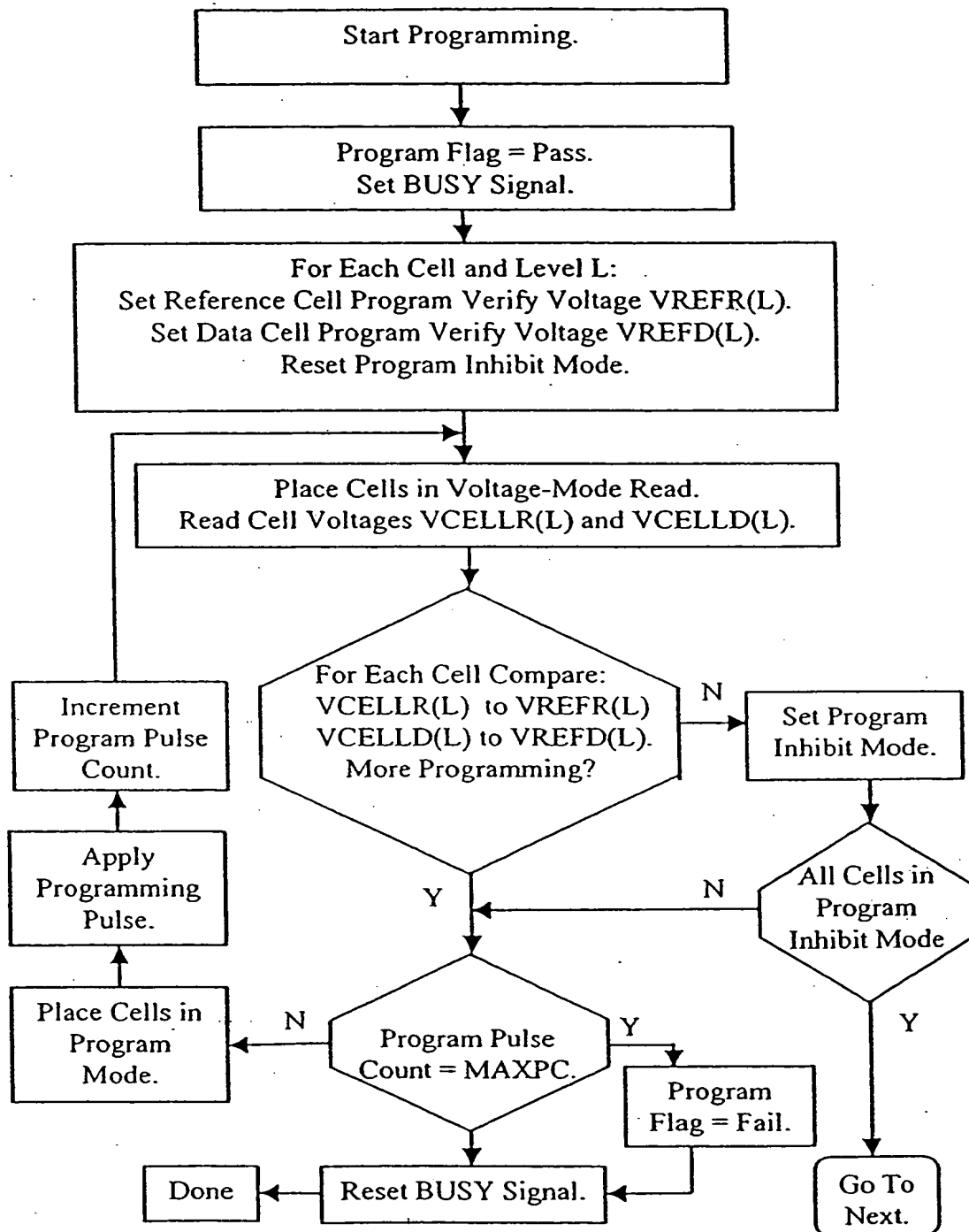


FIG. 21

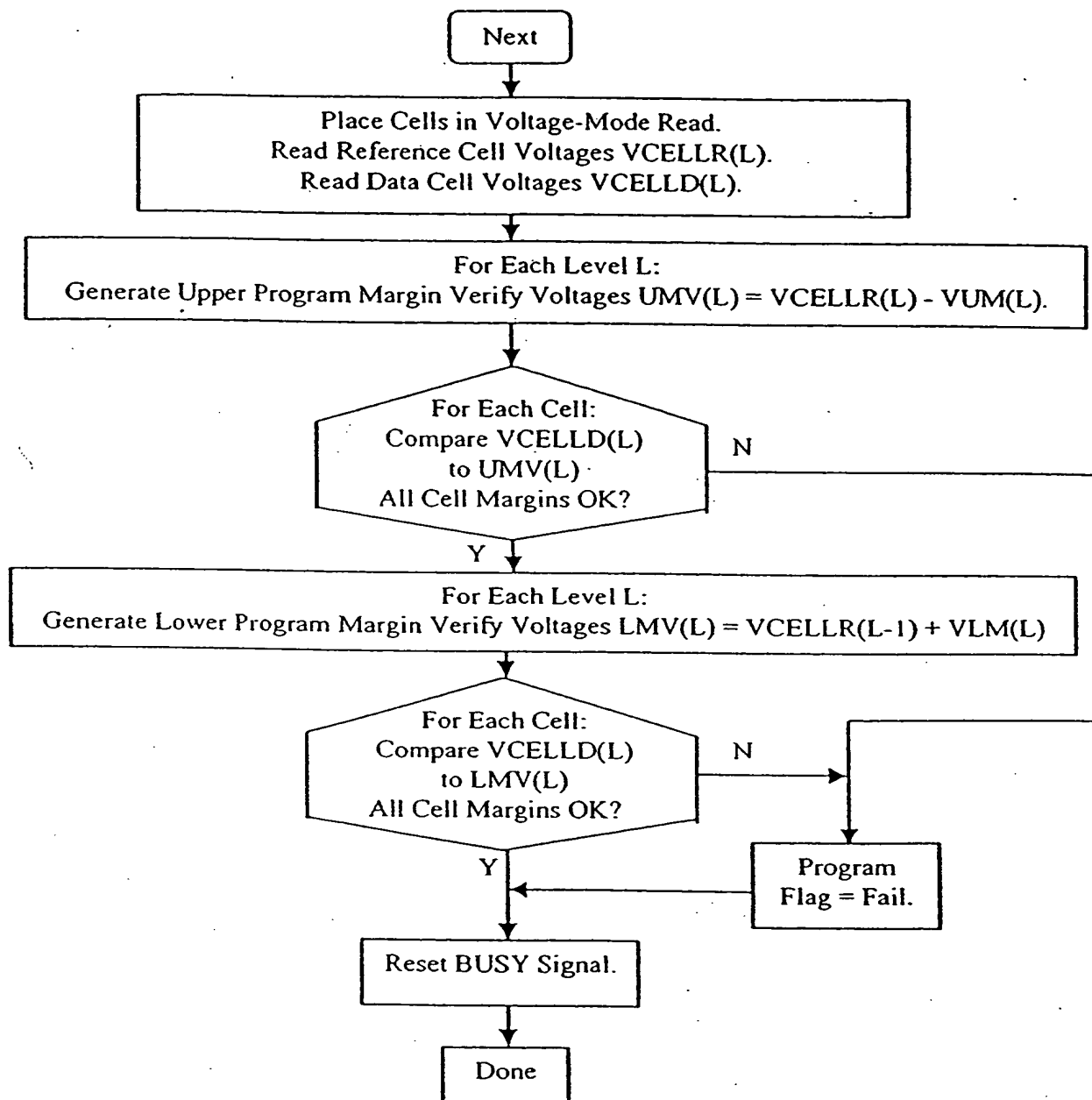


Fig. 22A

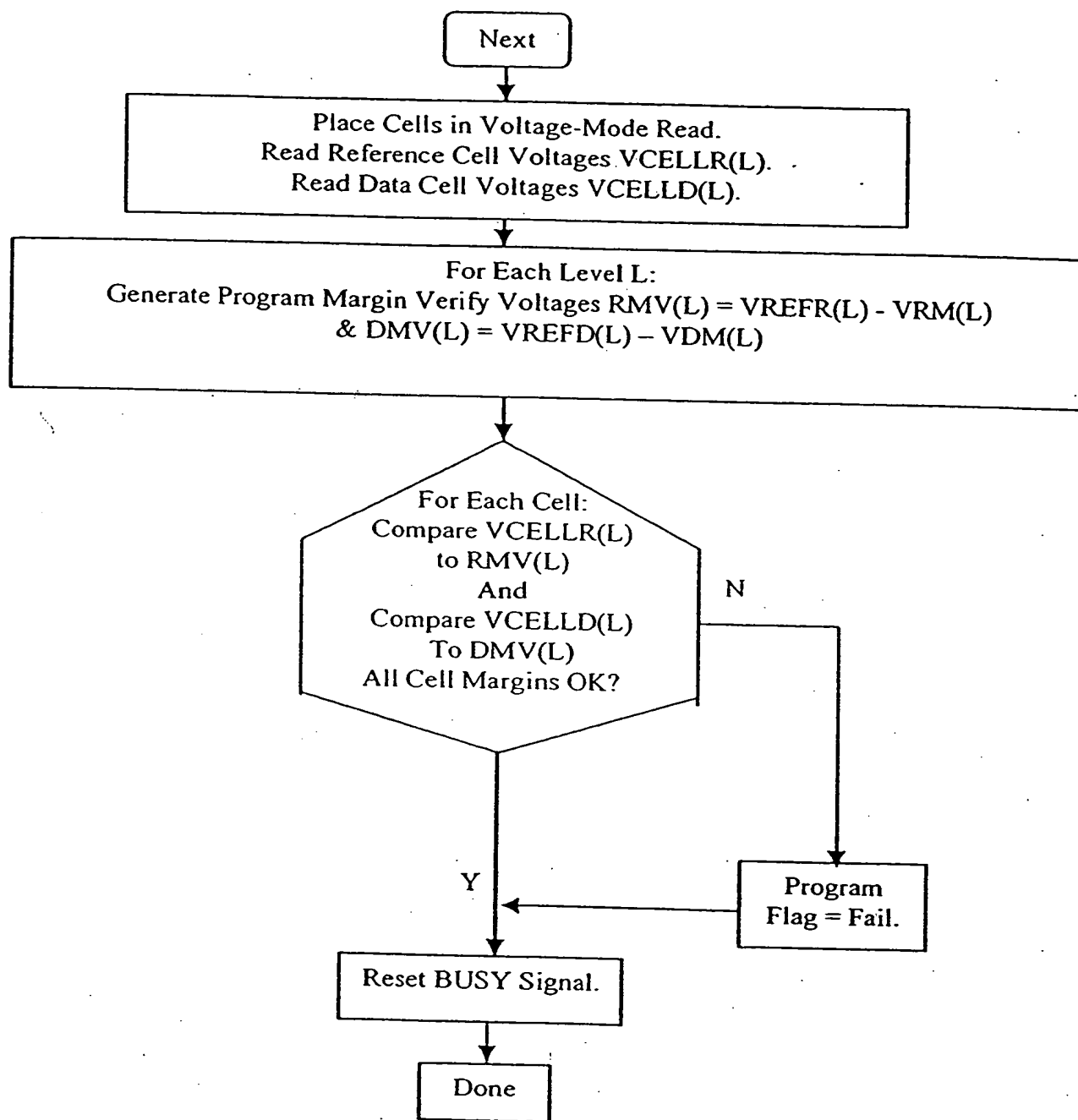


Fig. 22B

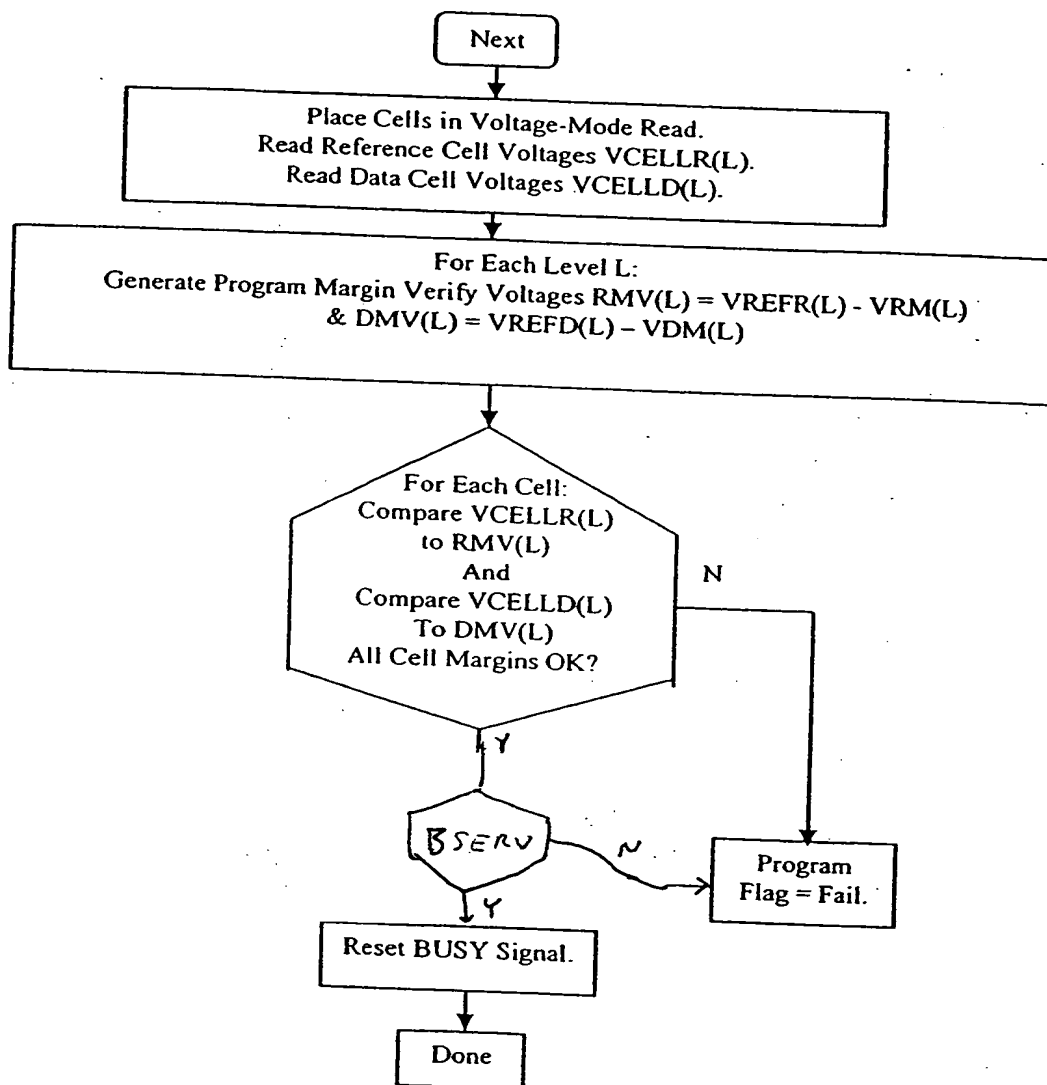


FIGURE 22C

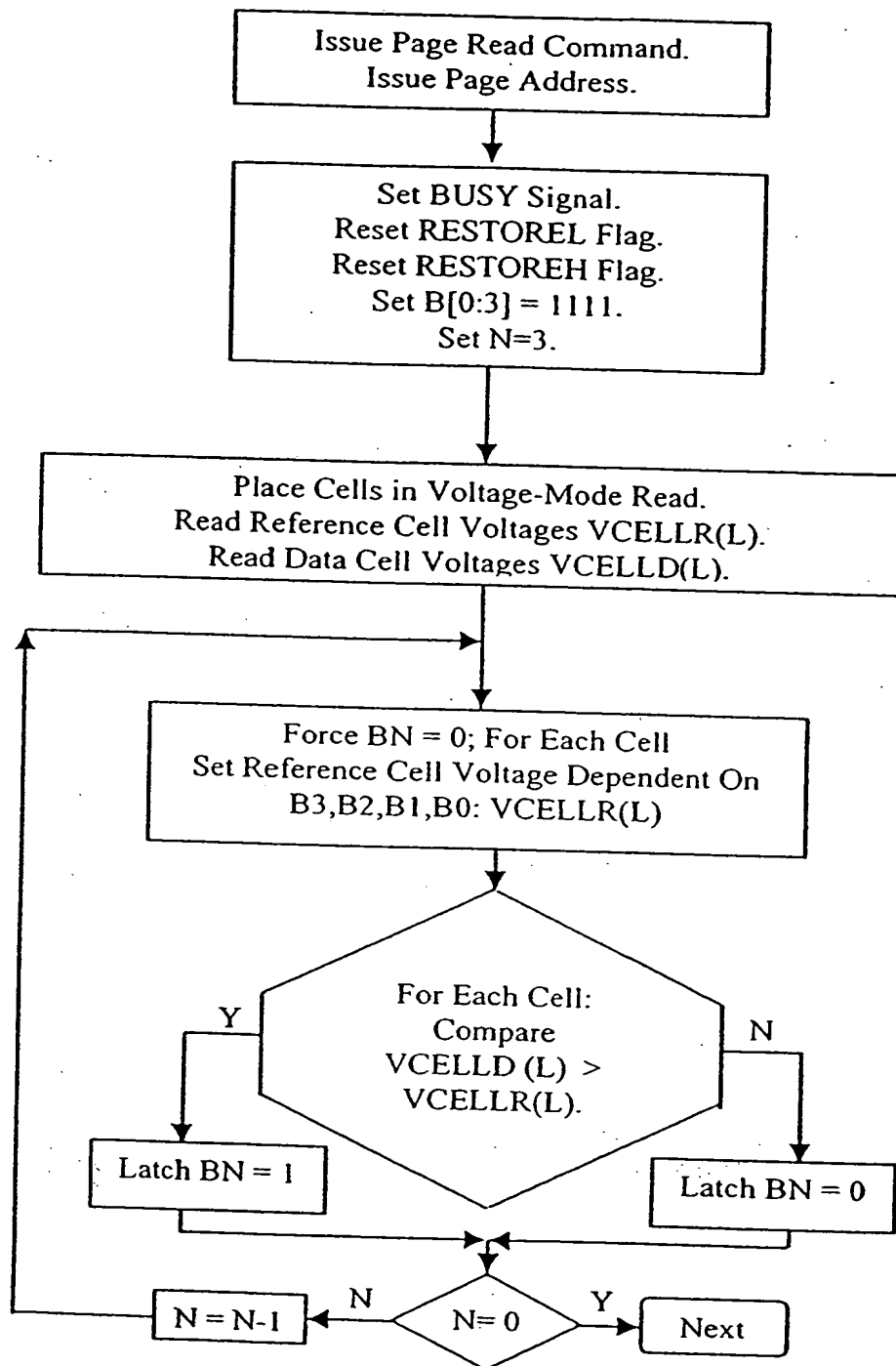


FIG. 23

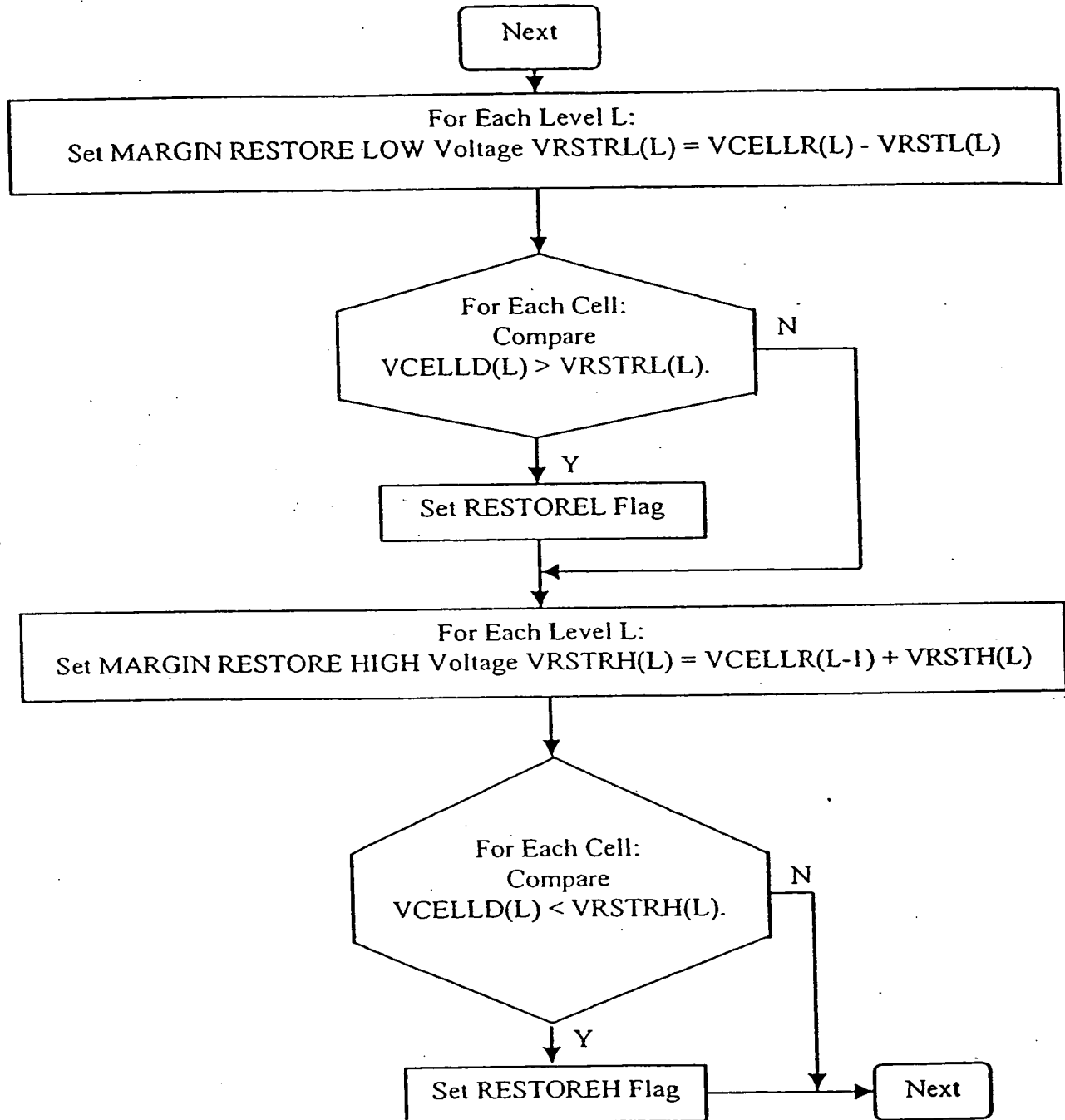


FIG. 24

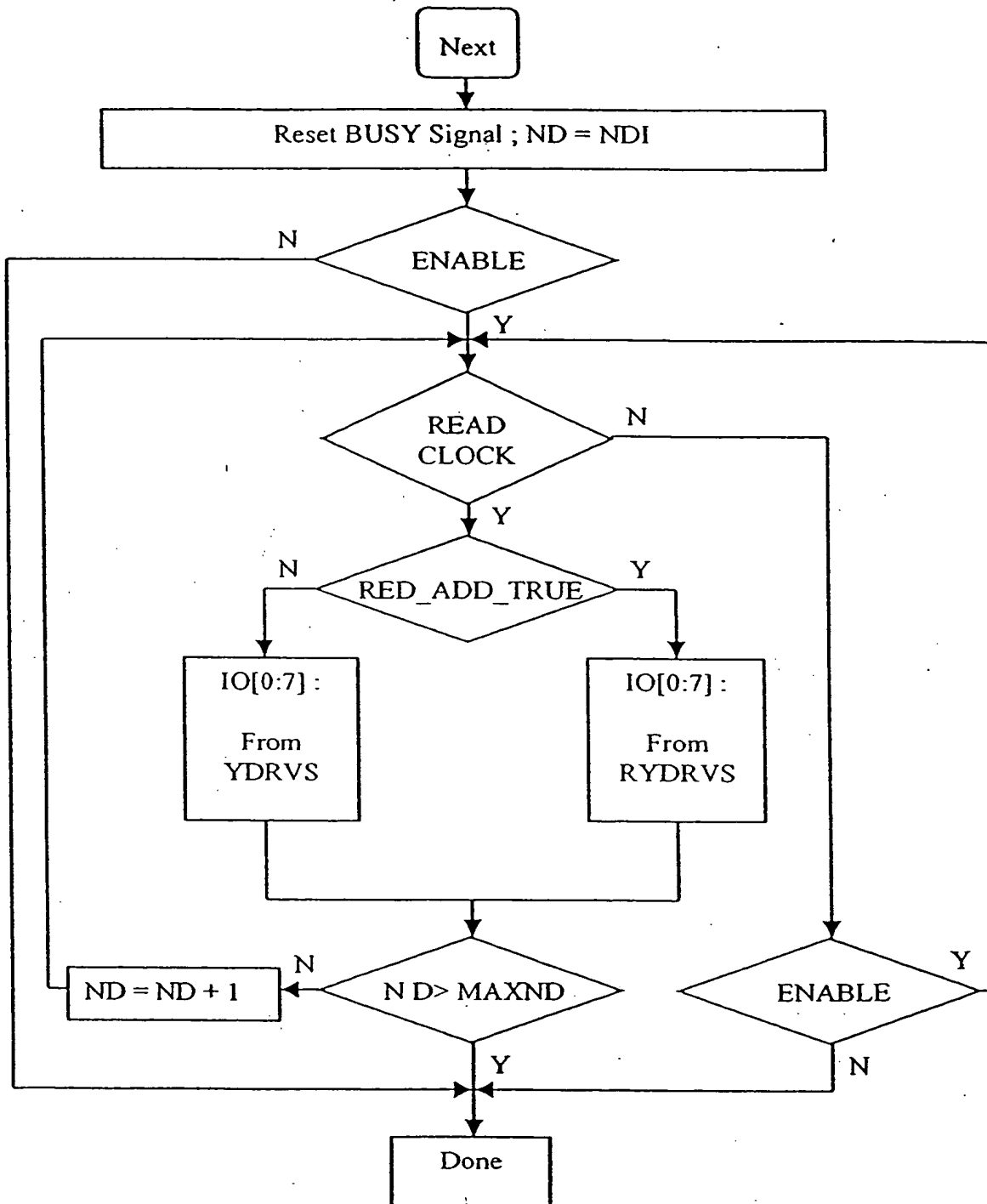


FIG. 25

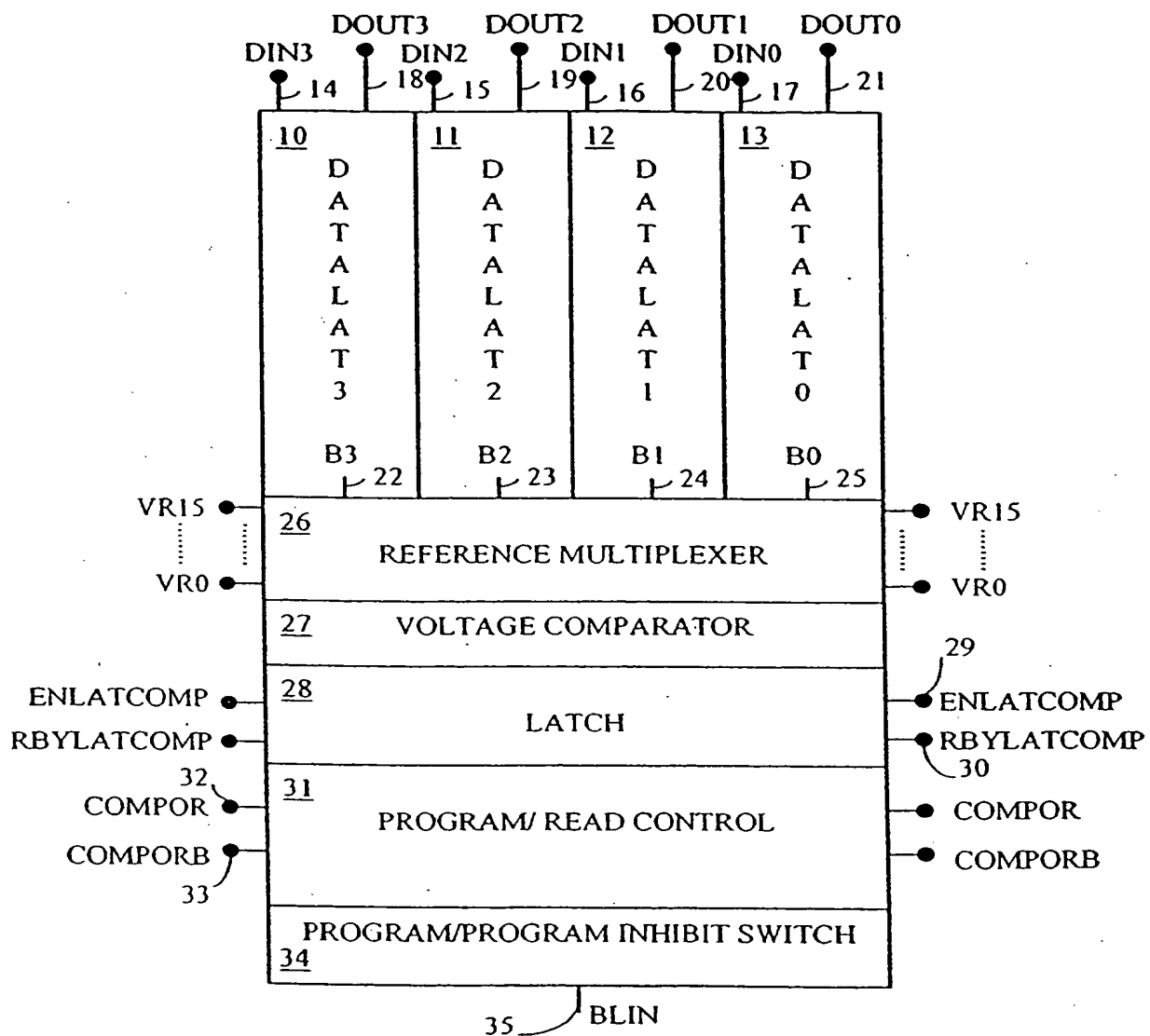


Fig. 26

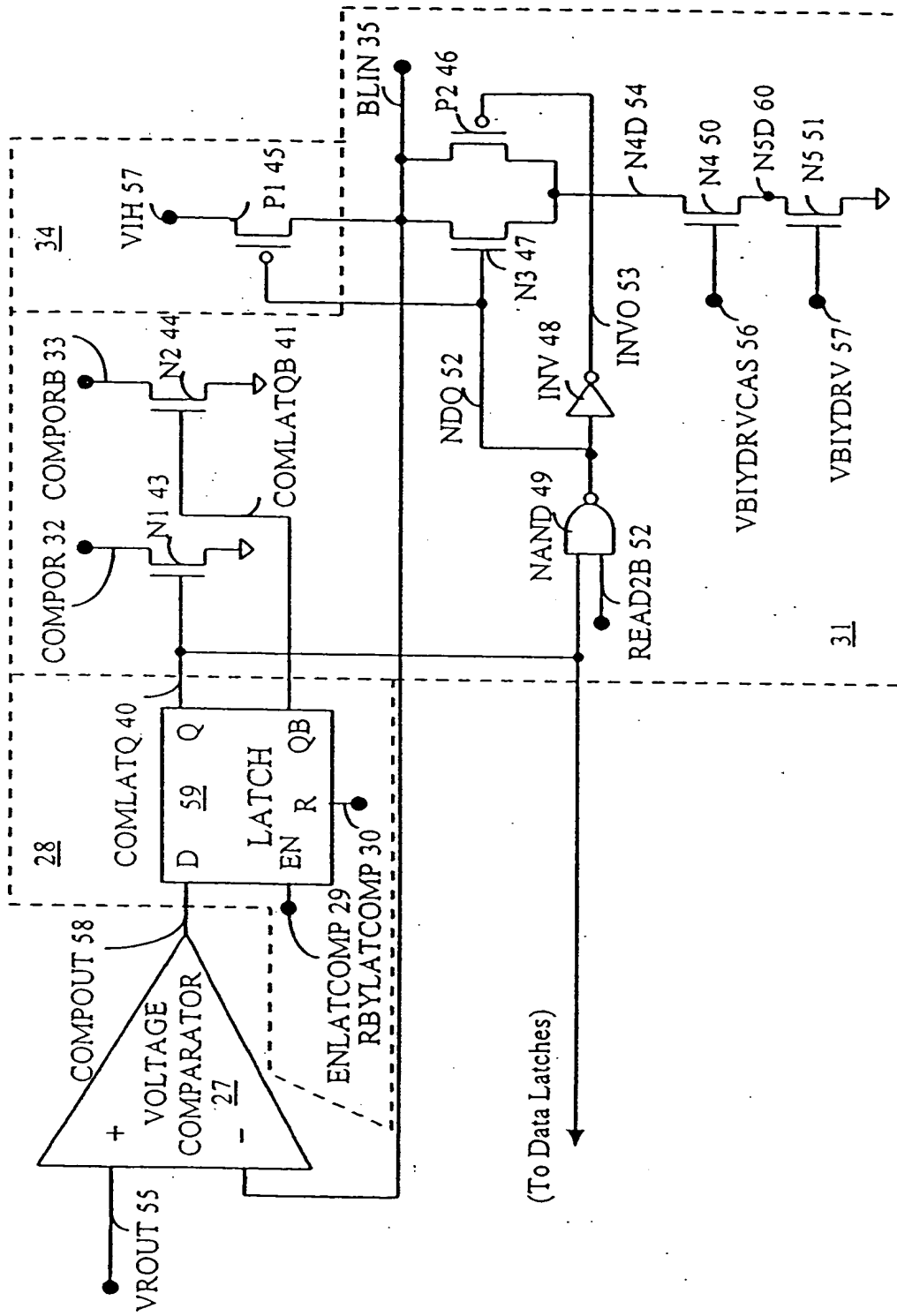
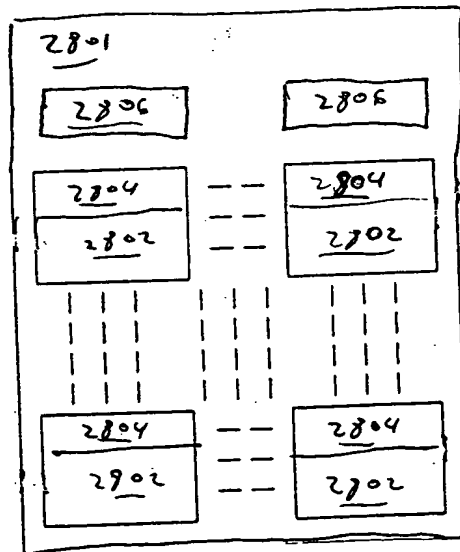
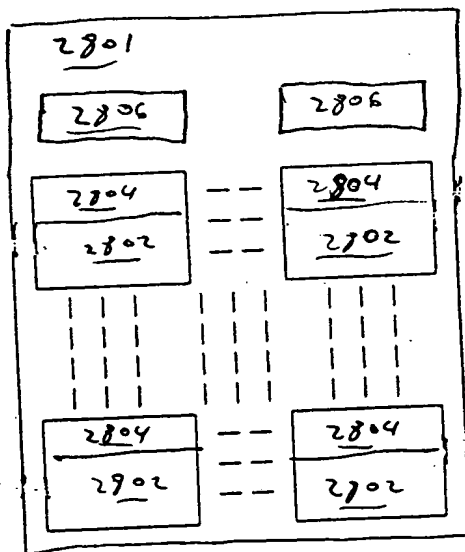
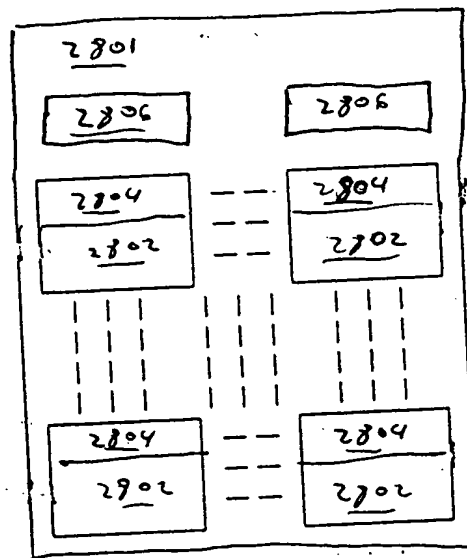
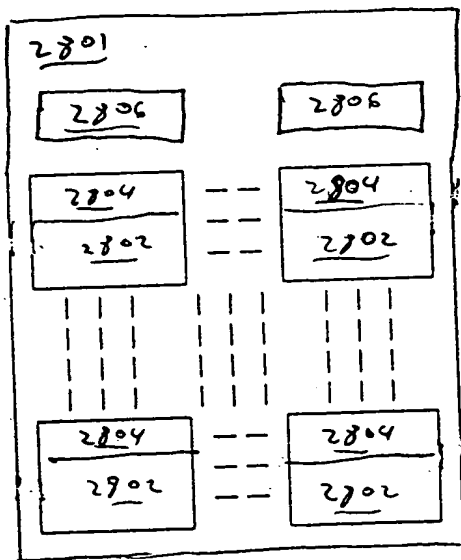


Fig. 27



2800

FIGURE 28

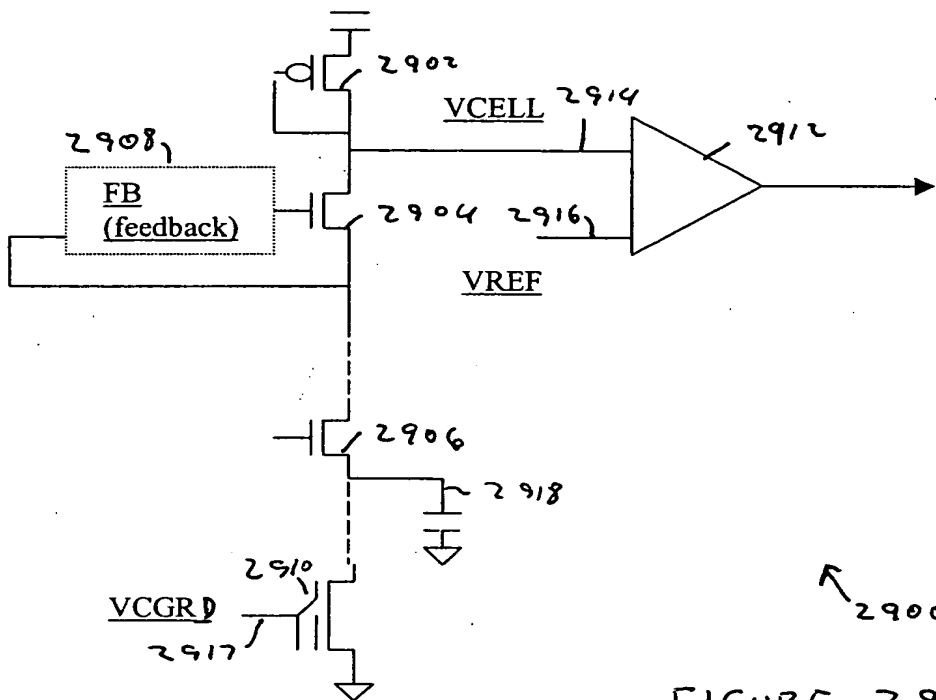


FIGURE 29A

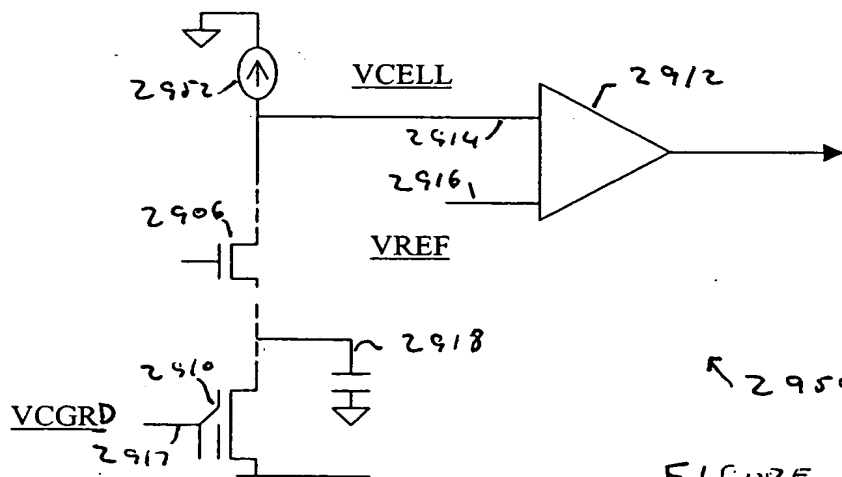


FIGURE 29B

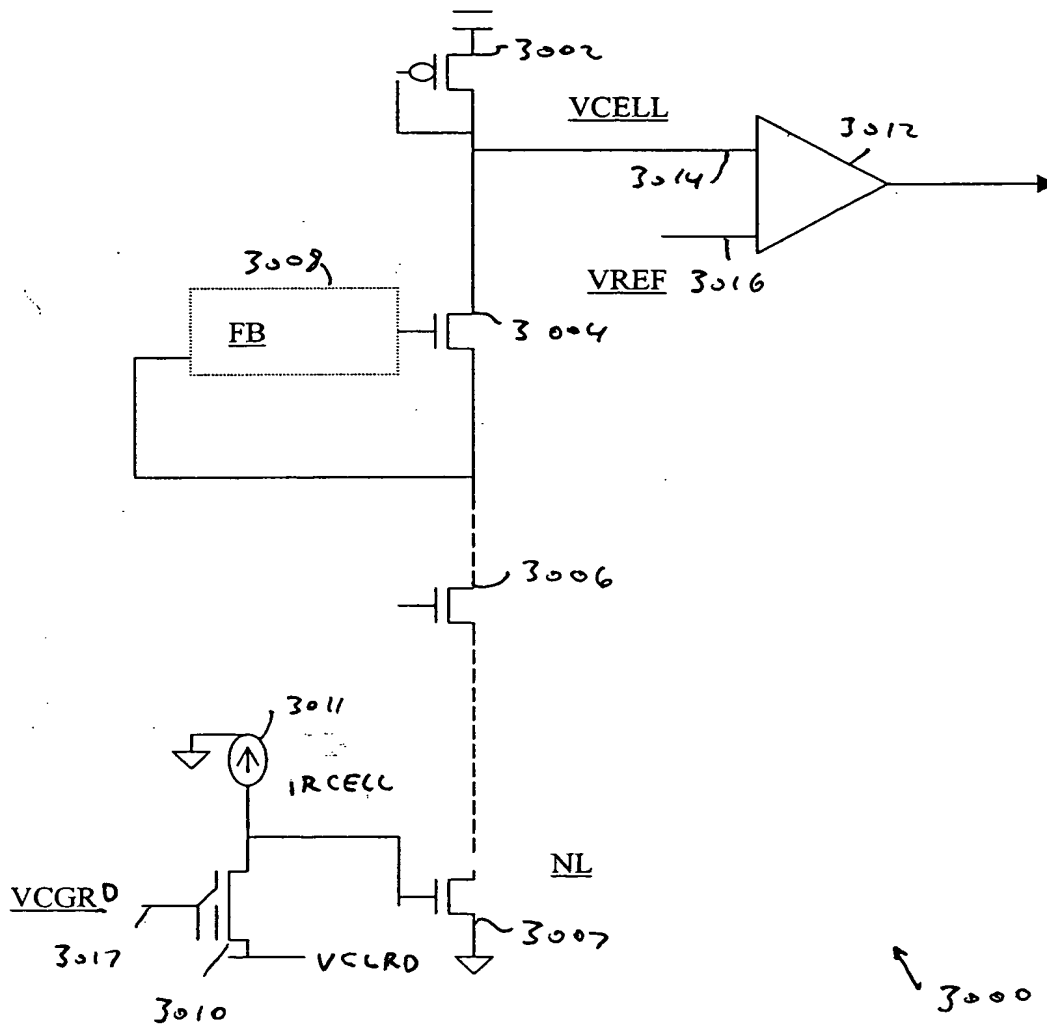


FIGURE 30

FIGURE 31

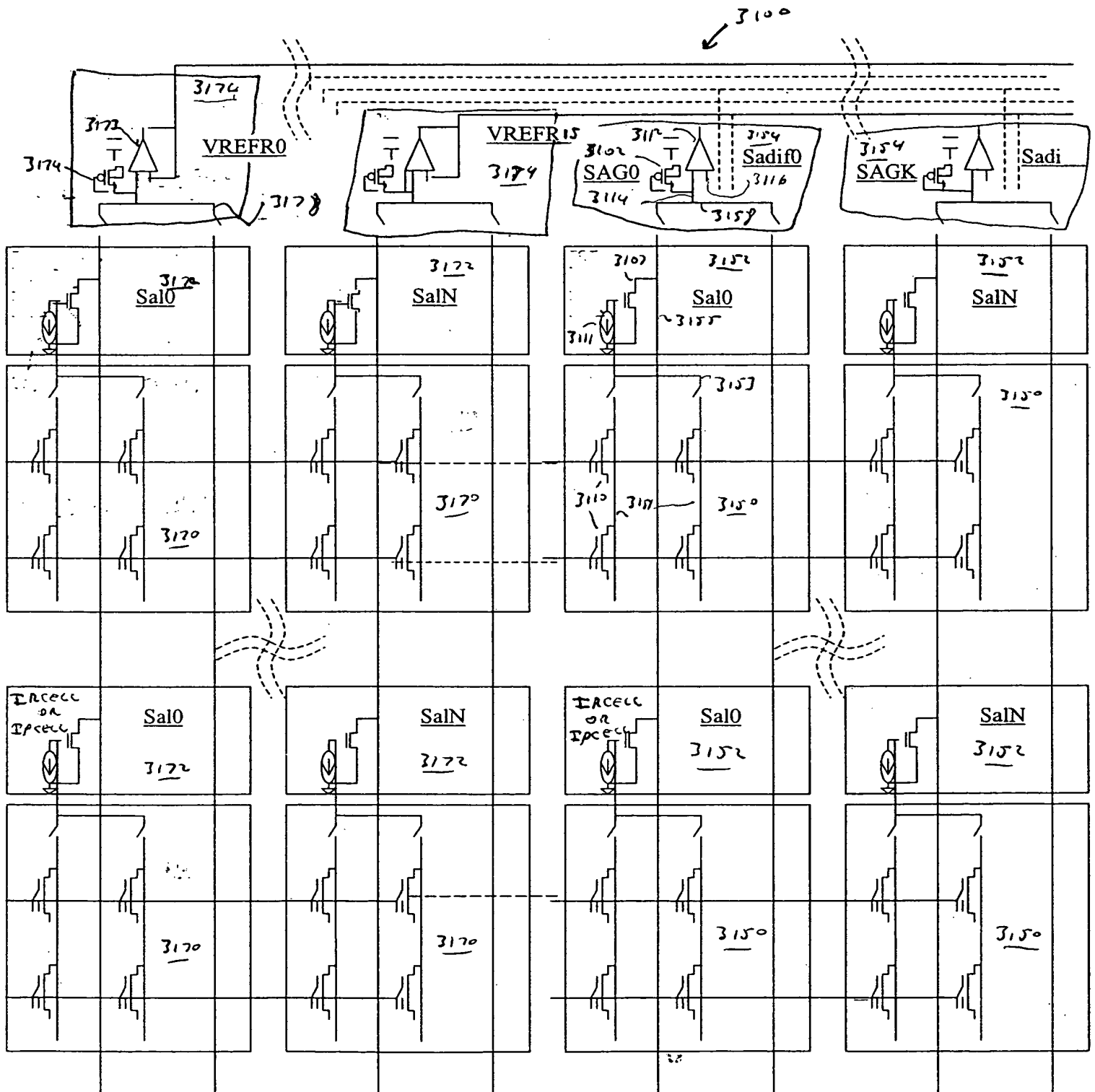


FIGURE 32

3200

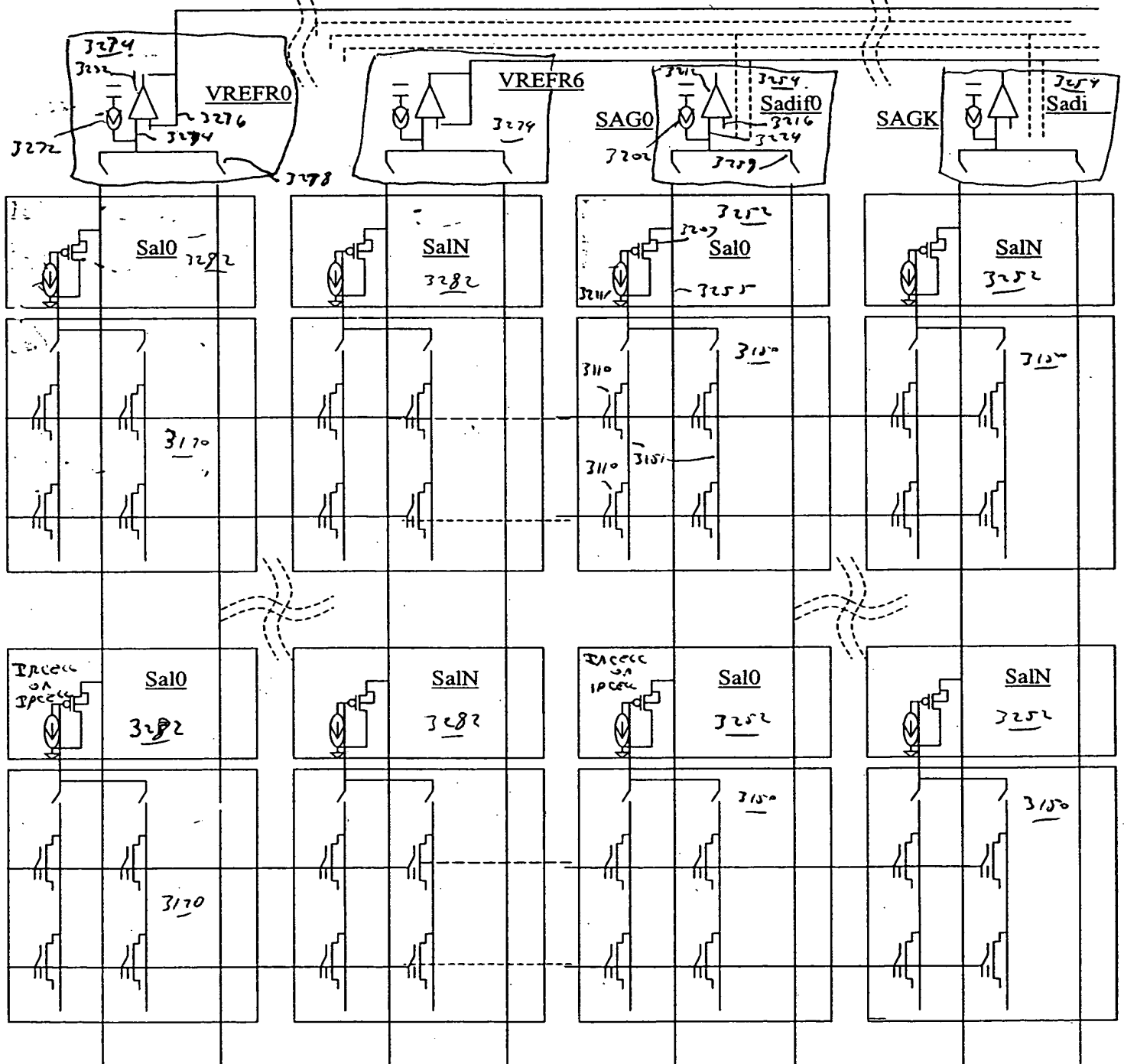
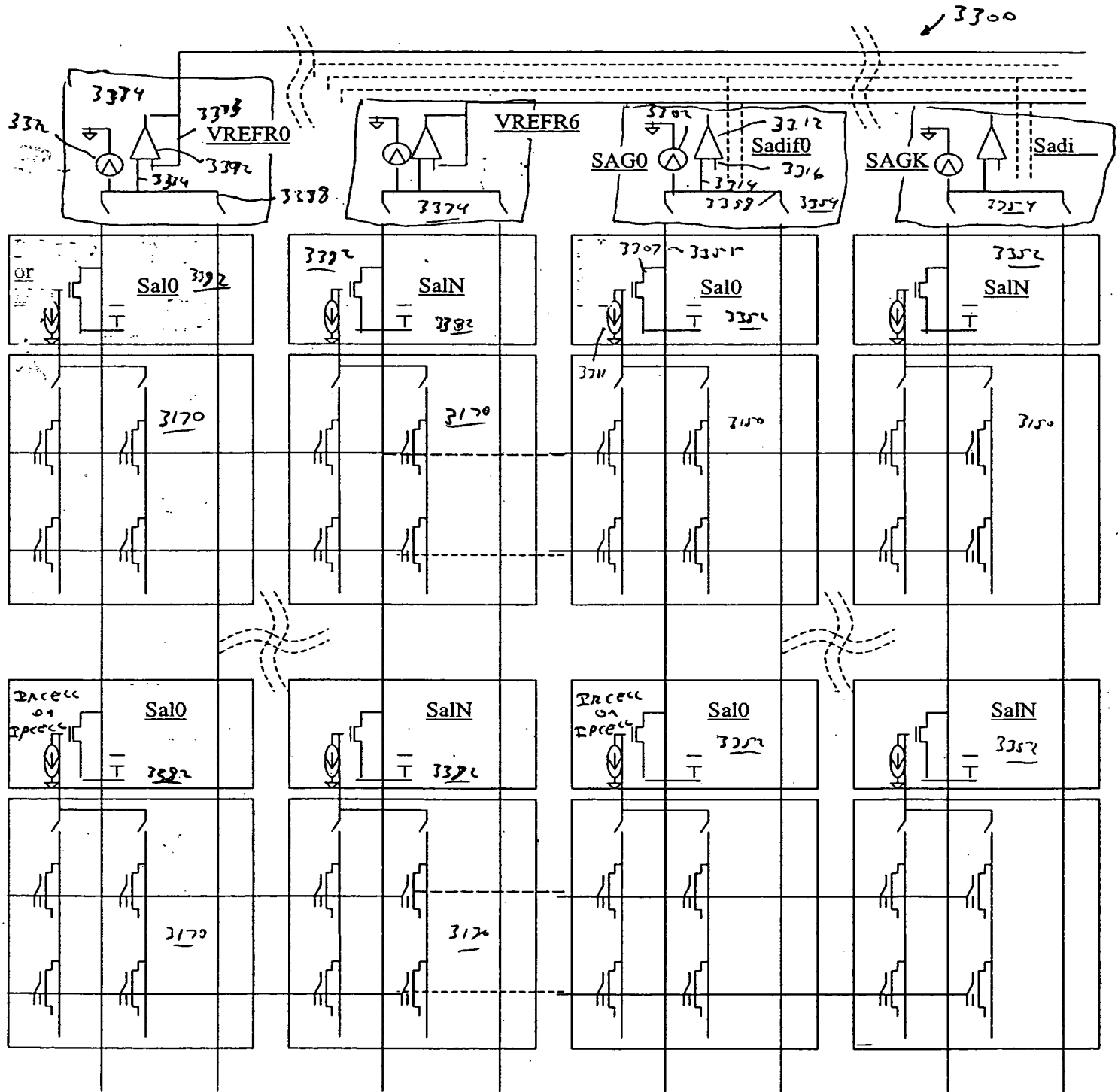


FIGURE 33



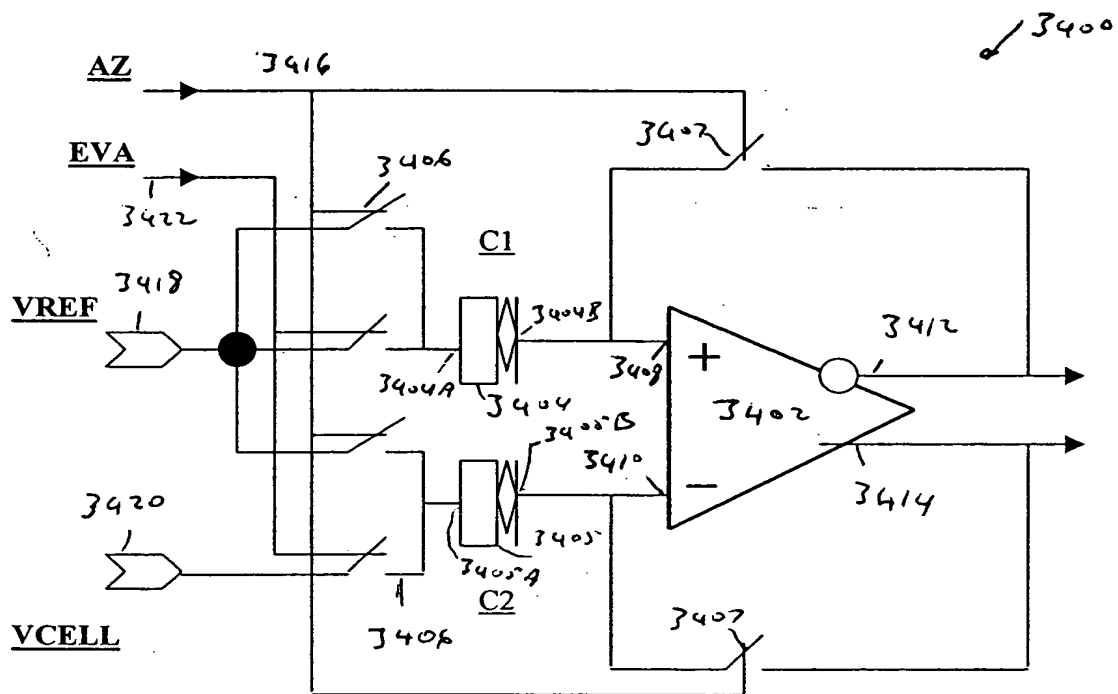


FIGURE 34

